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16	NPCE985E & FLASH	1A	
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20	USB	1A	
21	LAN RTL8111GS	1A	
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24	AUDIO(ALC233-CG)	1A	
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26	POWER +VCC_CORE (ISL95837)	1A	
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28	POWER 1.35VSUS/VTT_MEM	1A	
29	POWER +1.05V(G5602R41U)-15A	1A	
30	POWER VCCSA/VCCIO	1A	
31	POWER VCC1.8/Thermal	1A	
32	POWER(BAT IN / ADA IN/ UL)	1A	
33	POWER CHARGER (ISL88731C)	1A	
34	POWER VGA_CORE/1.0(RT8812A)	1A	
35	POWER VCC1.5_VRAM/1.05V	1A	
36	NVIDIA N14 GB2-64 PCIE 1/4	1A	
37	NVIDIA N14 GB2-64 TMDS 2/4	1A	
38	NVIDIA N14 GB2-64 VRAM 3/4	1A	
39	NVIDIA N14 GB2-64 VRAM 4/4	1A	

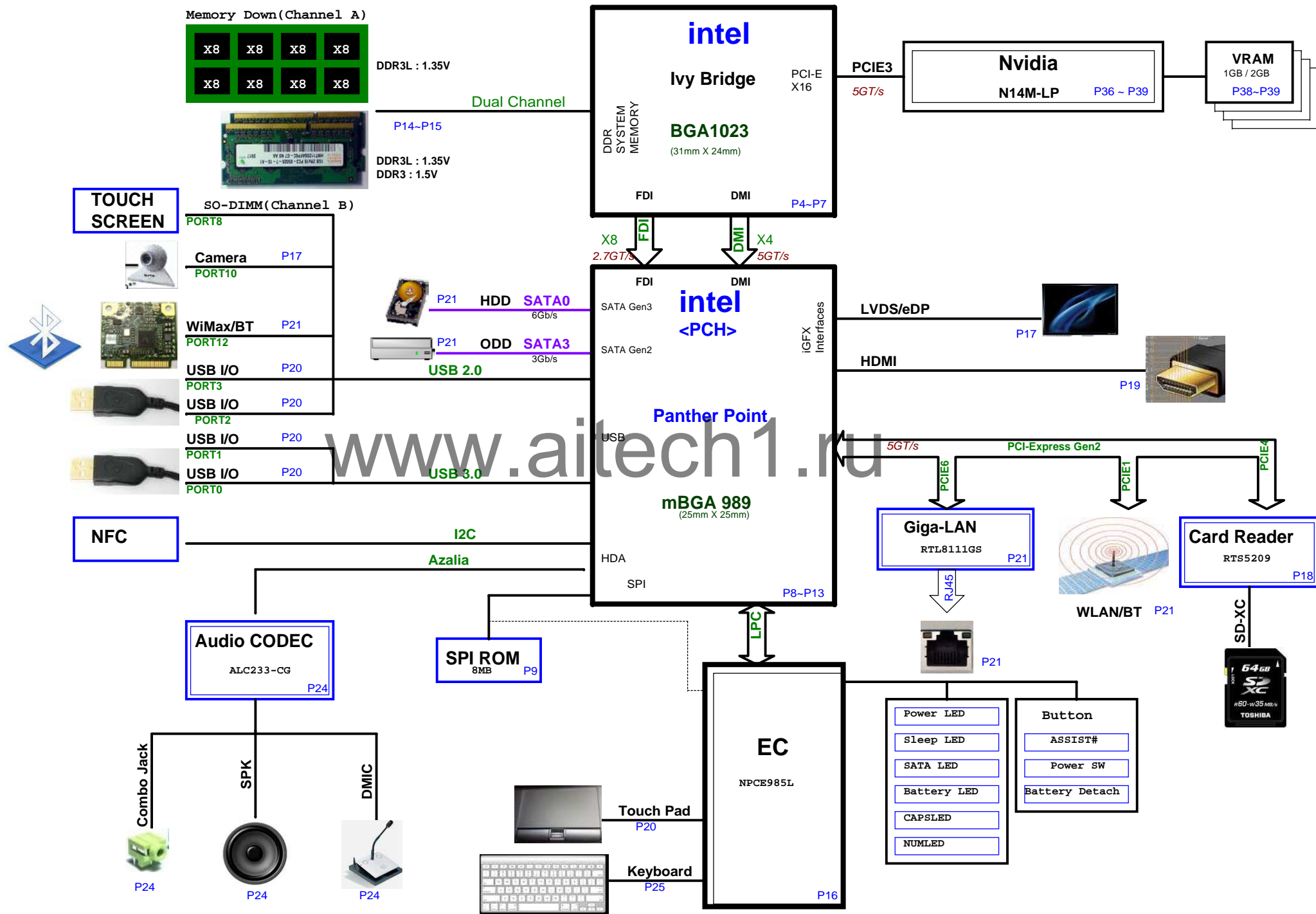
Page	Title of schematic page	Rev.	Date
40	HOLE/EMI/KB	1A	
41	IO PORT LIST	1A	

* : No mount
E@ : For DIS GFX only
I@ : For INT GFX only

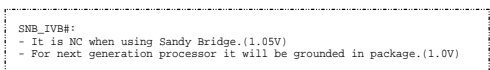
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Chief River ULV BLOCK DIAGRAM

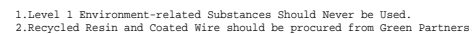
02

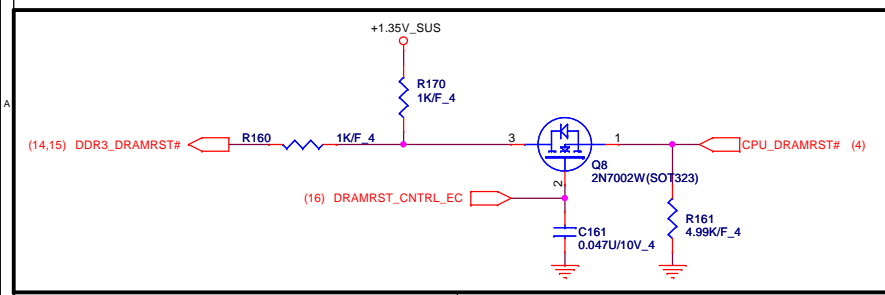


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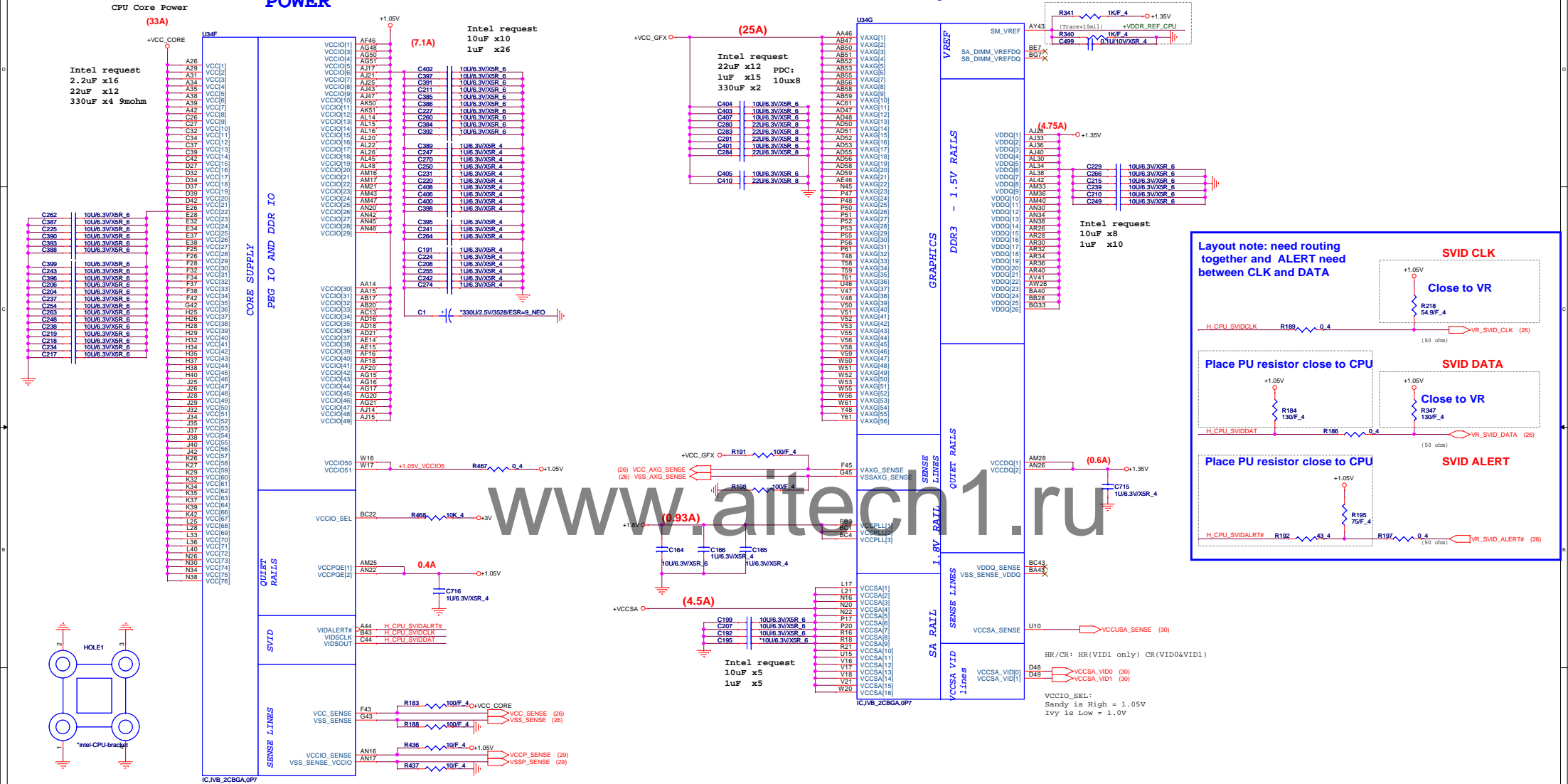


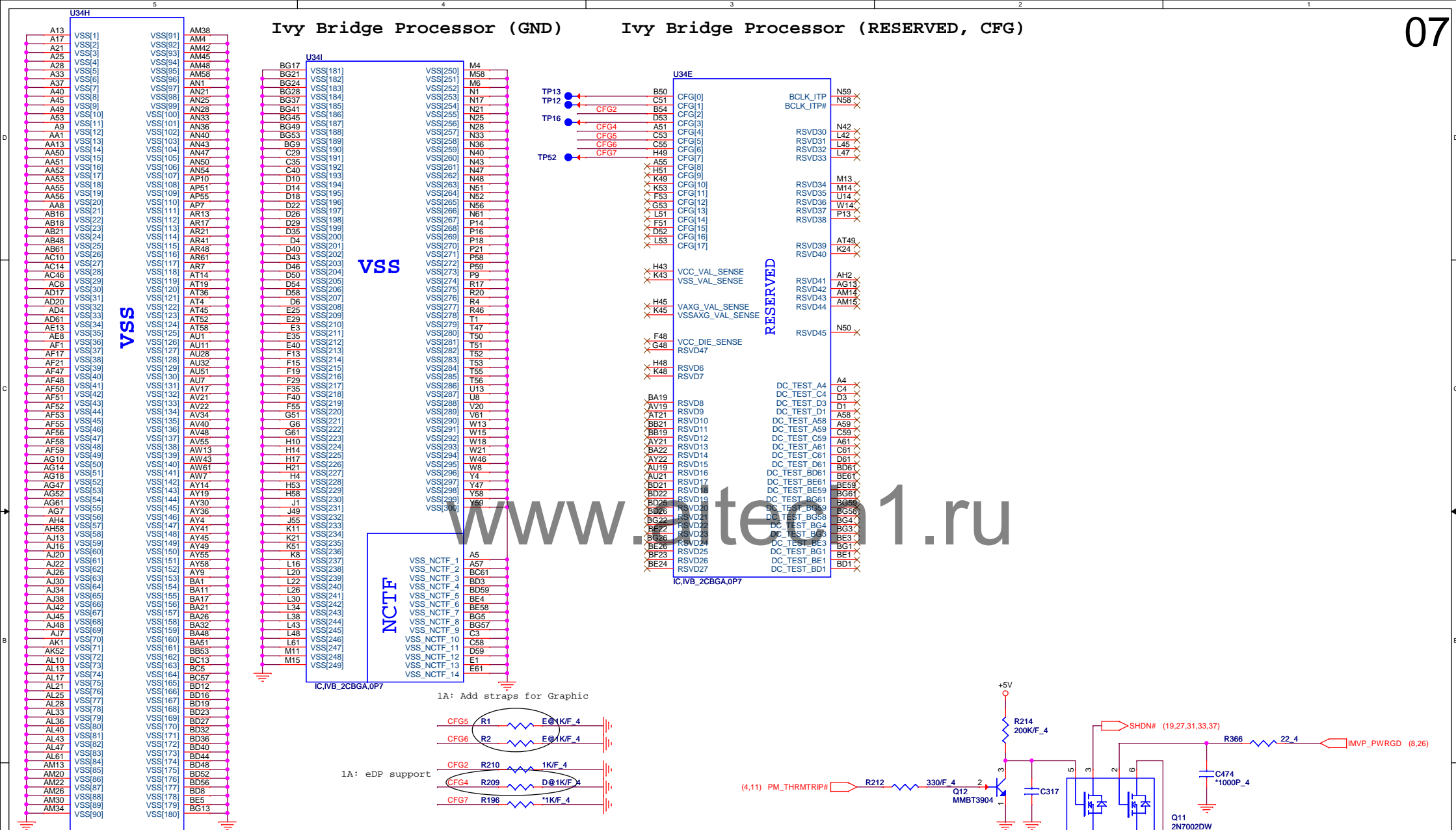
- FDI_FSYNC (J18/J17/J19/H17) can gang all these 4 signals together and tie them with only one 1k resistor to GND (DG V0.5 Ch2.2.9).
- FDI_INT connect to GND with 1k ohm.





POWER





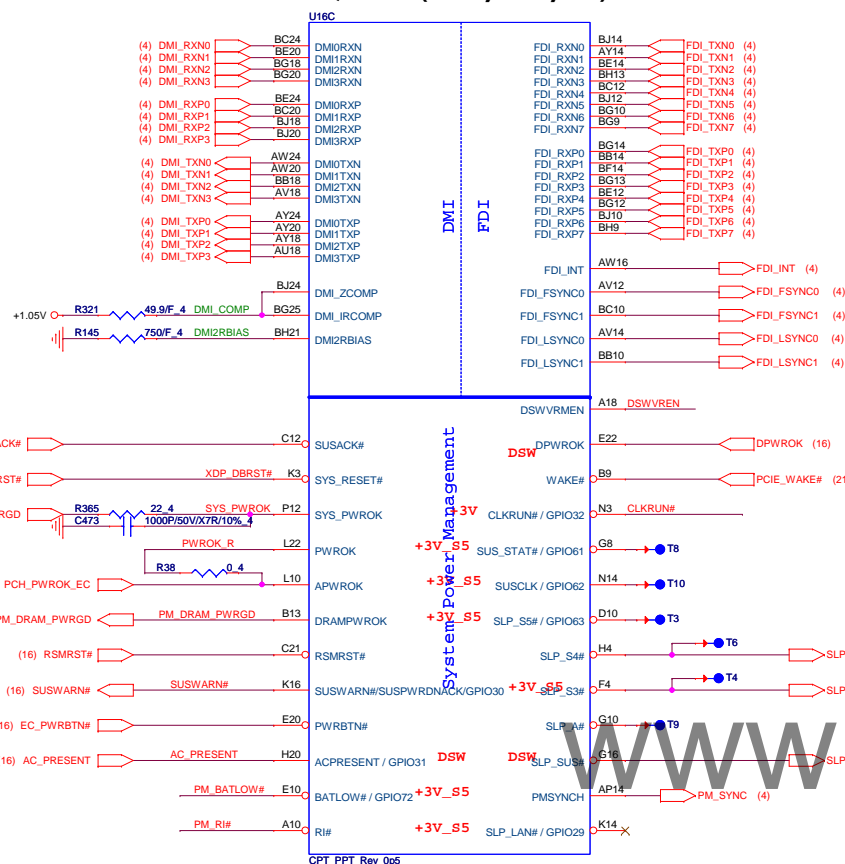
Processor Strapping

	1	0
CFG2 (PEG Static Lane Reversal)	Normal Operation	Lane Reversed
CFG4 (DP Presence Strap)	Disable; No physical DP attached to eDP	Enable; An ext DP device is connected to eDP
CFG7 (PEG Defer Training)	PEG train immediately following xxRESETB de assertion	PEG wait for BIOS training

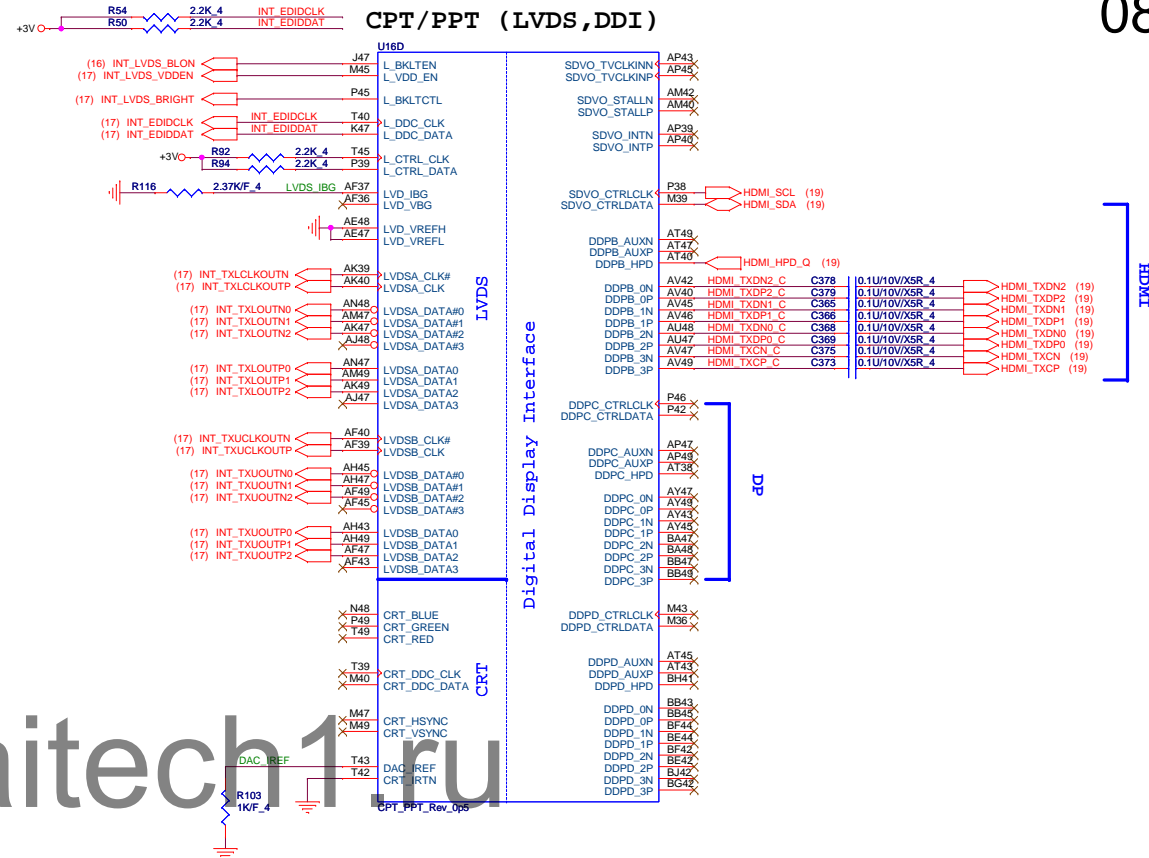
CFG[6:5] (PCIe Port Bifurcation Straps)

```
11: (Default) x16 - X16 PEG interface
10: PEG x8 x8 bifurcation enableddisabled
01: Reserved - (Device 1 function 1 disabled ; function 2 enabled)
00: x8,x4,x4 - Device 1 functions 1 and 2 enabled
```

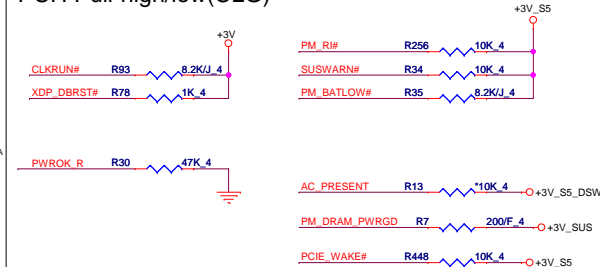
CPT/PPT (DMI, FDI, PM)



CPT/PPT (LVDS,DDI)

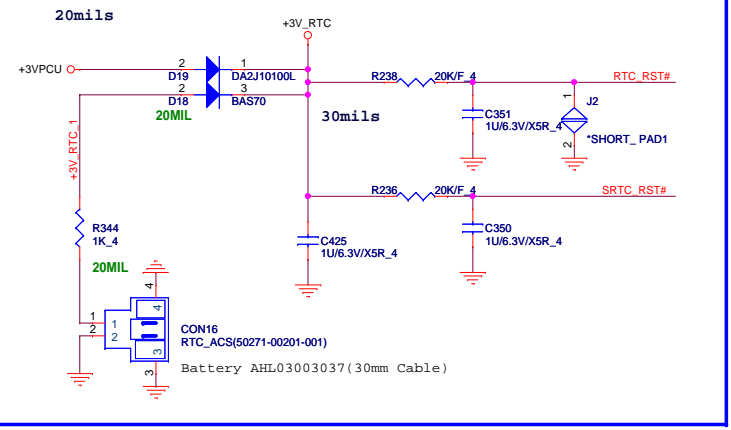


PCH Pull-high/low(CLG)



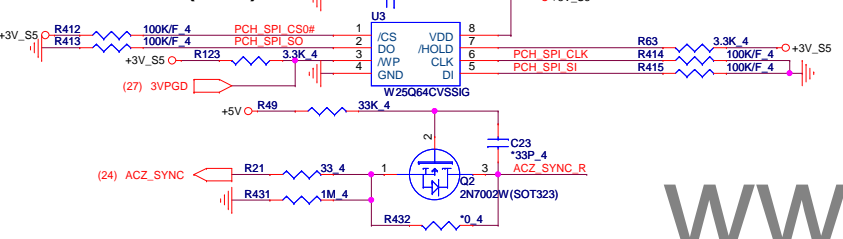
On Die DSW VR Enable
High = Enable (Default)
Low = Disable

RTC Circuitry(RTC)



MX25L3205DM2I-12G: AKE39FP0Z00
W25X32VSSIG: AKE39ZP0N00

PCH SPI (CLG)



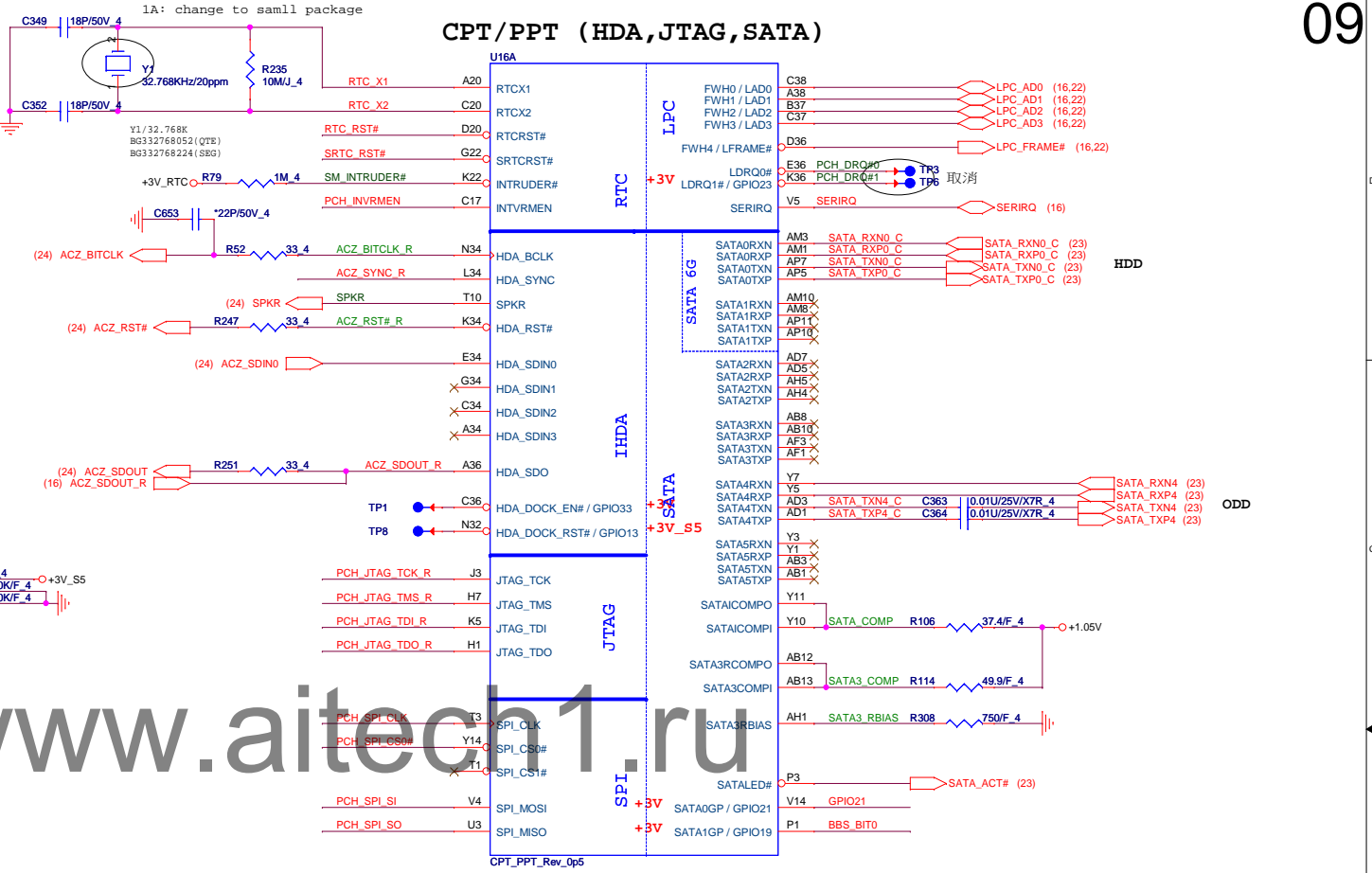
For NPCE885L Using

(16) F_CS0#_PCH	R121	0.4	PCH SPI CS0#
(16) F_SDI_PCH	R67	0.4	PCH SPI SO
(16) SCK_PCH	R65	0.4	PCH SPI CLK
(16) SD0_PCH	R122	0.4	PCH SPI SI

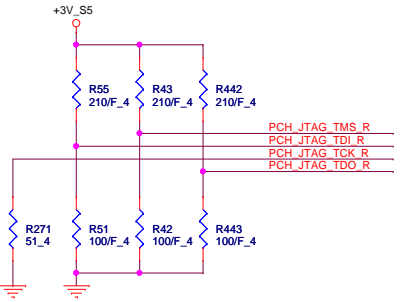
PCH Strap Table

Pin Name	Strap description	Sampled	Configuration	Note
SPKR	No reboot mode setting	PWROK	0 = Default (weak pull-down 20K) 1 = Setting to No-Reboot mode	SPKR
PCI_GNT3# / GPIO55	Top-Block Swap Override	PWROK	0 = "top-block swap" mode 1 = Default (weak pull-up 20K)	TP53
INTVRMEN	Integrated 1.05V VRM enable	ALWAYS	Should be always pull-up	+3V_RTC
GNT1# / GPIO51	Boot BIOS Selection 1 [bit-1]	PWROK	Default weak pull-up on GNT0/1# [Need external pull-down for LPC BIOS]	TP47
GPIO19	Boot BIOS Selection 0 [bit-0]	PWROK		TP43
HDA_SDO	Flash Descriptor Security	PWROK	0 = Default (weak pull-down 20K) 1 = Enabled	ACZ_SDOUT_R
DF_TVS	DMI/FDI Termination voltage	PWROK	0 = Set to Vss for Ivy Bridge 1 = Set to Vcc for Sandy Bridge (weak pull-down 20K)	TP44
GPIO28	On-die PLL Voltage Regulator	RSMRST#	0 = Disable 1 = Enable (Default)	TP44
HDA_SYNC	On-Die PLL VR Voltage Select	RSMRST	0 = Support by 1.8V (weak pull-down) 1 = Support by 1.5V	TP44

CPT/PPT (HDA, JTAG, SATA)



PCH JTAG Debug (CLG)

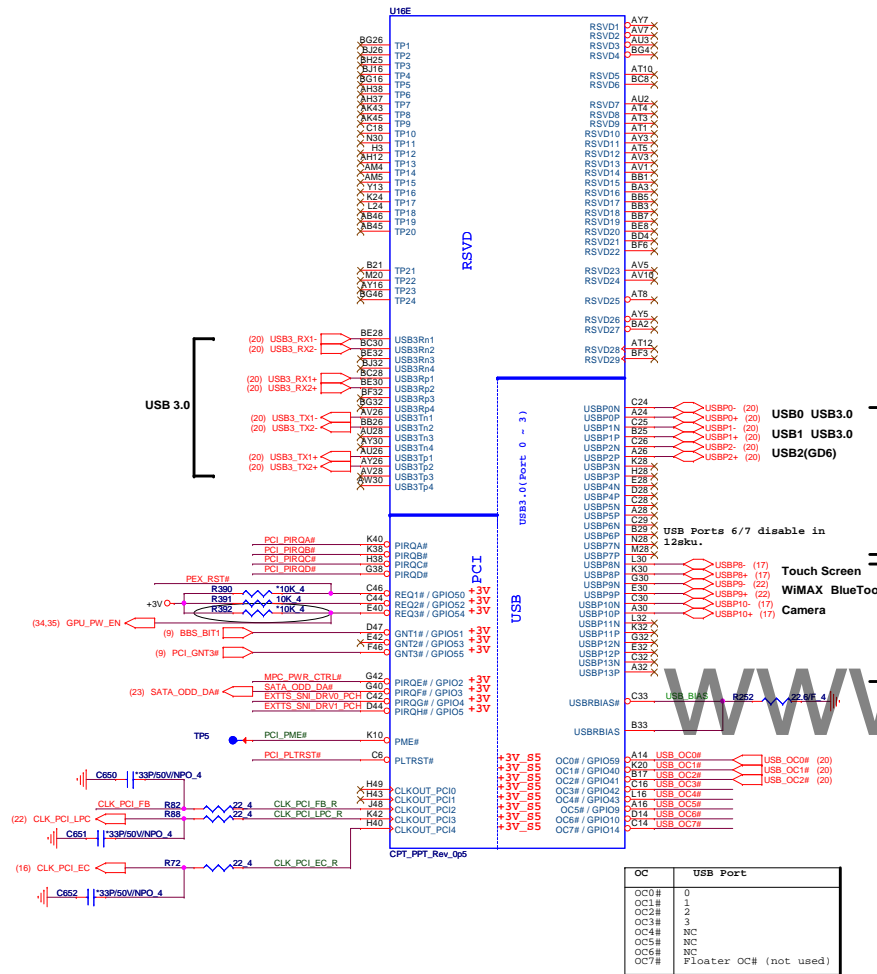


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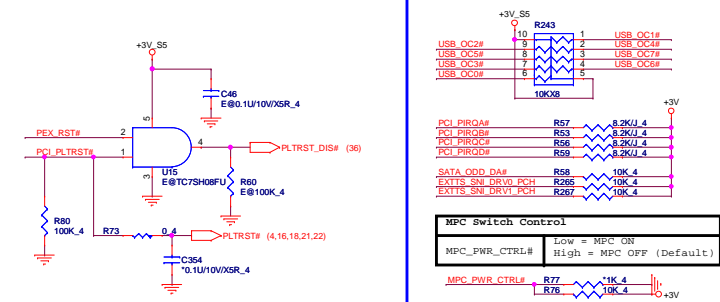
Pin 100s

Signal	Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal	Pin
WLAN	(22) PCIE_RXN1	BQ34	PERN1	+3V_S5	SMBALERT# / GPIO11				
	(22) PCIE_RXP1	B334	PERP1		SMBCLK				
	(22) PCIE_TXN1	A332	PETN1		SMBDATA				
	(22) PCIE_TXP1	A332	PETP1						
Card	(18) PCIE_RXN2_CARD	B834	PERN2	+3V_S5	SMLDAlert# / GPIO60				
	(18) PCIE_RXP2_CARD	B832	PERP2		SMLCLK				
	(18) PCIE_TXN2_CARD	A332	PETN2		SMLDATA				
	(18) PCIE_TXP2_CARD	A332	PETP2						
LAN	(21) PCIE_RXN3_LAN	BQ36	PERN3						
	(21) PCIE_RXP3_LAN	A334	PERP3						
	(21) PCIE_TXN3_LAN	A334	PETN3						
	(21) PCIE_TXP3_LAN	A334	PETP3						
		B336	PERN4	+3V_S5	SML1Alert# / PCHHOT# / GPIO74				
		B334	PERP4		+3V_S5	SML1CLK / GPIO58			
		A336	PETN4		+3V_S5	SML1DATA / GPIO75			
		A336	PETP4						
		BQ37	PERN5						
		B337	PERP5						
		A337	PETN5						
		A337	PETP5						
		B338	PERN6						
		A338	PERP6						
		A338	PETN6						
		A338	PETP6						
		BQ40	PERN7						
		B340	PERP7						
		A340	PETN7						
		A340	PETP7						
		B341	PERN8						
		A341	PERP8						
		A341	PETN8						
		A341	PETP8						
WLAN	(22) CLK_PCIE_WLAN#	Y35	CLKOUT_PCIE0N	+3V_S5	PEG_A_CLKRQ# / GPIO47				
	(22) CLK_PCIE_WLAN#	Y35	CLKOUT_PCIE0P		CLKOUT_PEG_A_N				
		J2	PCIECLKRQ0# / GPIO73		CLKOUT_PEG_A_P				
Card	(18) CLK_PCIE_CARDN	AB49	CLKOUT_PCIE1N		CLKOUT_DMI_N				
	(18) CLK_PCIE_CARDP	AB47	CLKOUT_PCIE1P		CLKOUT_DMI_P				
		M1	PCIECLKRQ1# / GPIO18	+3V	CLKOUT_DP_N				
		AA48	CLKOUT_PCIE2N		CLKOUT_DP_P				
		AA47	CLKOUT_PCIE2P		CLKIN_DMI_N				
		V10	PCIECLKRQ2# / GPIO20	+3V	CLKIN_DMI_P				
		X37	CLKOUT_PCIE3N		CLKIN_GMD1_N				
		X36	CLKOUT_PCIE3P		CLKIN_GMD1_P				
		A5	PCIECLKRQ3# / GPIO25	+3V_S5	CLKIN_DOT_96N				
		X43	CLKOUT_PCIE4N		CLKIN_DOT_96P				
		X42	CLKOUT_PCIE4P		CLKIN_SATA_N				
		L12	PCIECLKRQ4# / GPIO28	+3V_S5	CLKIN_SATA_P				
		V40	CLKOUT_PCIE5N		REFCLK14N				
		X40	CLKOUT_PCIE5P		CLKIN_PCILOOPBACK				
		L14	PCIECLKRQ5# / GPIO44	+3V_S5					
		AB42	CLKOUT_PEG_B_N						

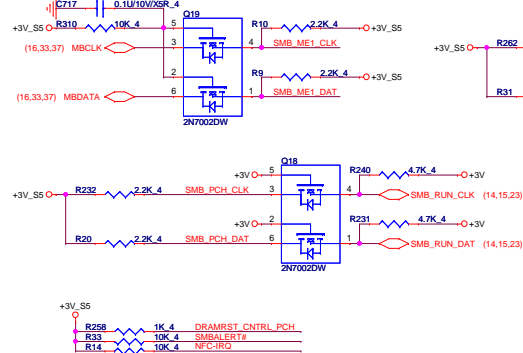
For EC



PCI/USB OC# Pull-up (CLG)



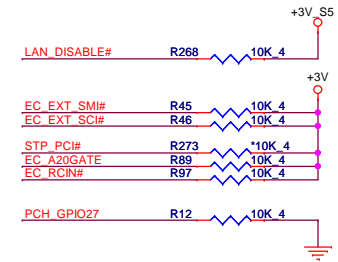
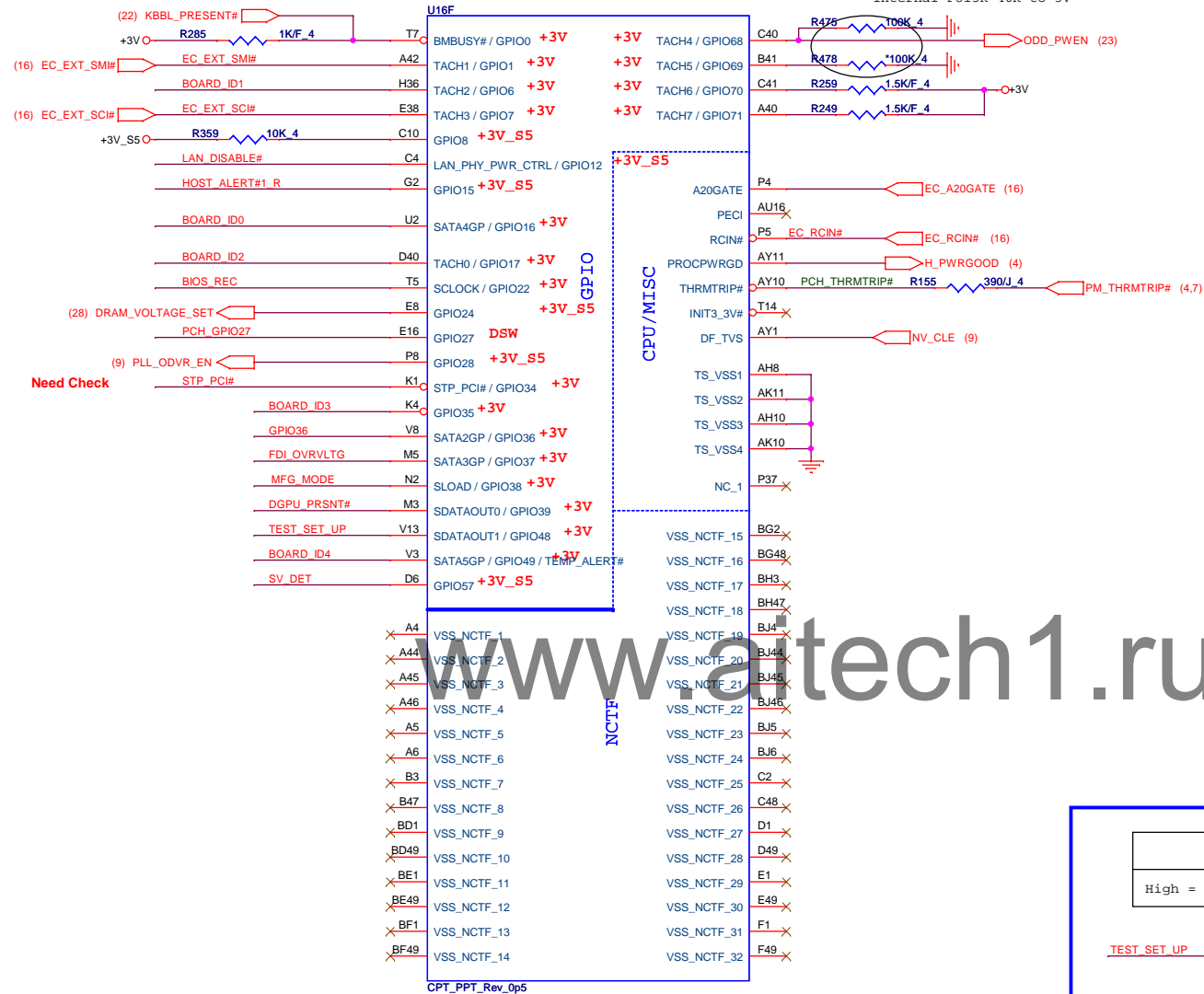
SMBus/Pull-up(CLG)



CPT/PPT (GPIO,VSS_NCTF,RSVD)

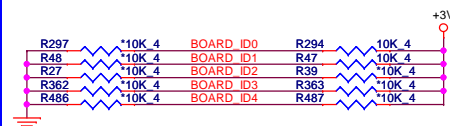
GPIO Pull-up/Pull-down(CLG)

11

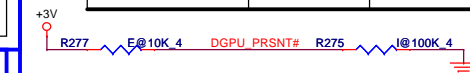


	0	1
Board ID0	CaspicCRA1/CRB1 HK8/9	SuperiorCRA1/CRB1 GD5/6
Board ID1	14"	15"

	Board ID2	Board ID3	Board ID4
SAM 2G	0	0	1
SAM 4G	0	1	0
HYN 2G	0	1	1
HYN 4G	1	0	0
ELP 2G	1	0	1
ELP 4G	1	1	0



PCBA SKU	Discrete	UMA
R277(Pull High)	Stuff	No Stuff
R275(Pull Low)	No Stuff	Stuff



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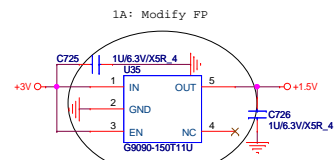
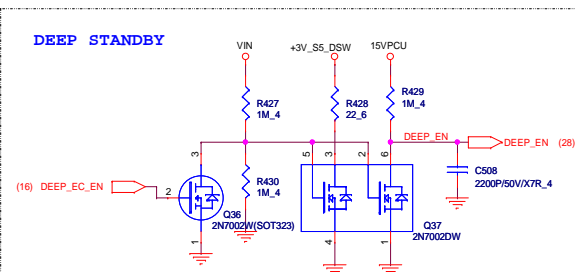
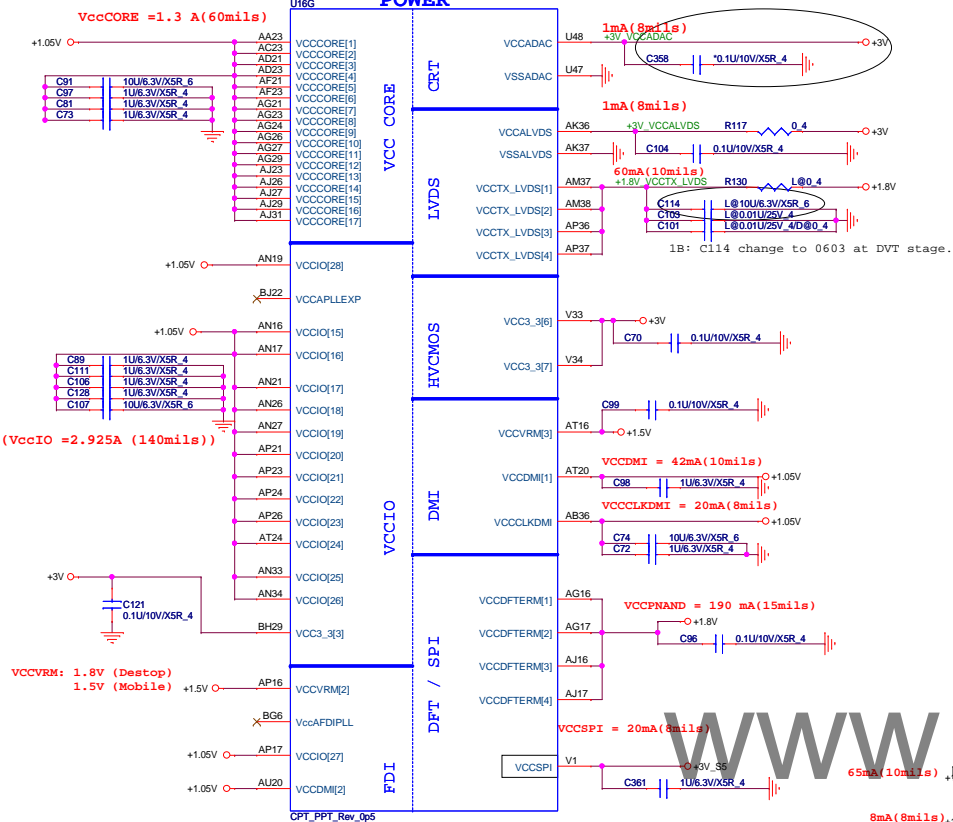
Size	Document Number	Rev
	CPT/PPT 4/6	1A
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PCH5 (CLG)

CPT/PPT (POWER)

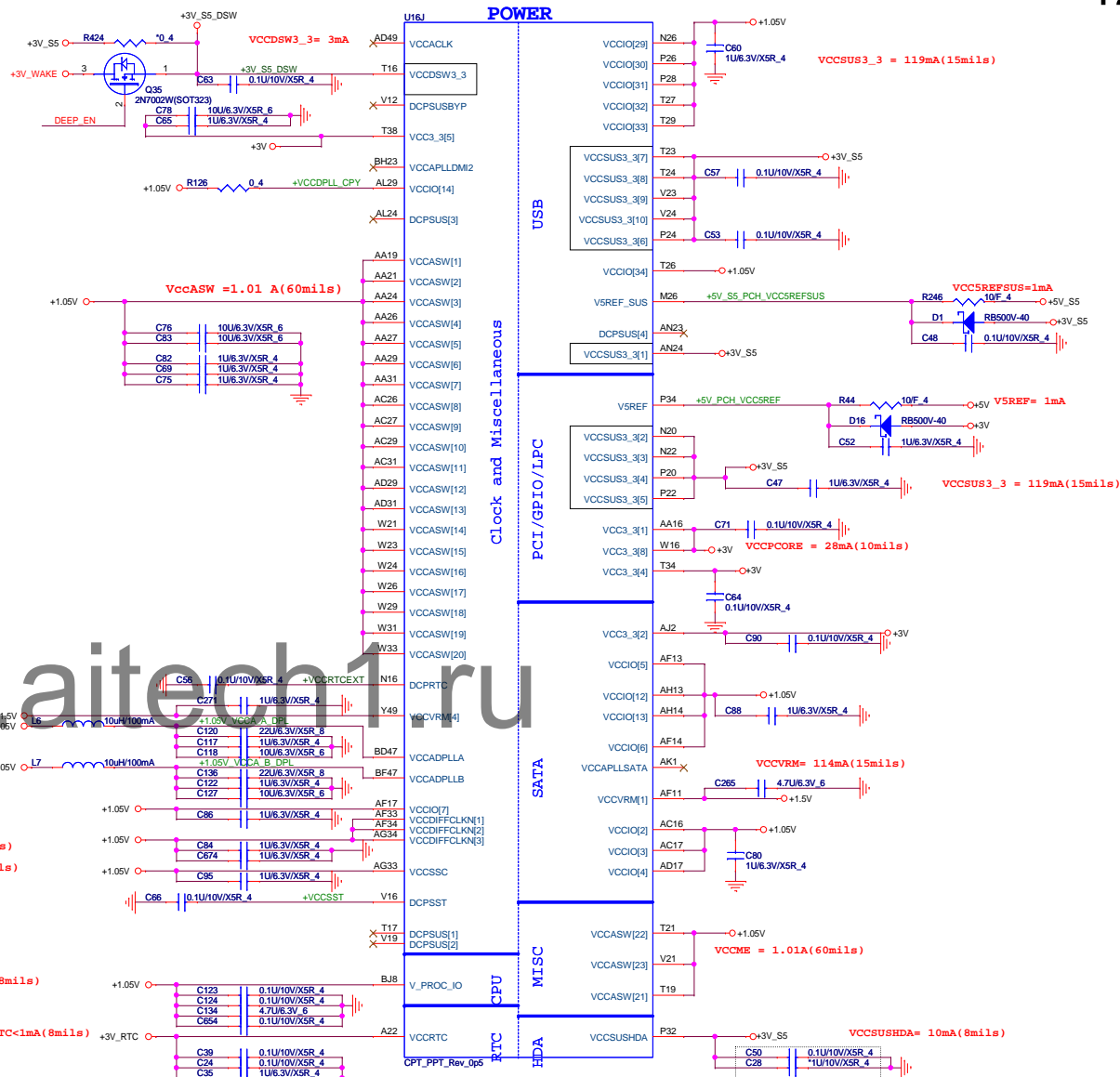
POWER

1A: CRT Disable



CPT/PPT (POWER)

POWER



CPT/PPT (GND)

U16I

H5	U16H	
AA17	VSS[0]	
AA2	VSS[1]	VSS[80]
AA3	VSS[2]	VSS[81]
AA33	VSS[3]	VSS[82]
AA34	VSS[4]	VSS[83]
AB11	VSS[5]	VSS[84]
AB14	VSS[6]	VSS[85]
AB39	VSS[7]	VSS[86]
AB4	VSS[8]	VSS[87]
AB43	VSS[9]	VSS[88]
AB5	VSS[10]	VSS[89]
AB7	VSS[11]	VSS[90]
AC19	VSS[12]	VSS[91]
AC2	VSS[13]	VSS[92]
AC21	VSS[14]	VSS[93]
AC24	VSS[15]	VSS[94]
AC33	VSS[16]	VSS[95]
AC34	VSS[17]	VSS[96]
AC48	VSS[18]	VSS[97]
AD10	VSS[19]	VSS[98]
AD11	VSS[20]	VSS[99]
AD12	VSS[21]	VSS[100]
AD13	VSS[22]	VSS[101]
AD19	VSS[23]	VSS[102]
AD24	VSS[24]	VSS[103]
AD26	VSS[25]	VSS[104]
AD27	VSS[26]	VSS[105]
AD33	VSS[27]	VSS[106]
AD34	VSS[28]	VSS[107]
AD36	VSS[29]	VSS[108]
AD37	VSS[30]	VSS[109]
AD38	VSS[31]	VSS[110]
AD39	VSS[32]	VSS[111]
AD4	VSS[33]	VSS[112]
AD40	VSS[34]	VSS[113]
AD42	VSS[35]	VSS[114]
AD43	VSS[36]	VSS[115]
AD45	VSS[37]	VSS[116]
AD46	VSS[38]	VSS[117]
AD6	VSS[39]	VSS[118]
AE2	VSS[40]	VSS[119]
AE3	VSS[41]	VSS[120]
AF10	VSS[42]	VSS[121]
AF12	VSS[43]	VSS[122]
AD14	VSS[44]	VSS[123]
AD16	VSS[45]	VSS[124]
AF16	VSS[46]	VSS[125]
AF19	VSS[47]	VSS[126]
AF24	VSS[48]	VSS[127]
AF26	VSS[49]	VSS[128]
AF27	VSS[50]	VSS[129]
AF29	VSS[51]	VSS[130]
AF31	VSS[52]	VSS[131]
AF38	VSS[53]	VSS[132]
AF4	VSS[54]	VSS[133]
AF42	VSS[55]	VSS[134]
AF46	VSS[56]	VSS[135]
AF5	VSS[57]	VSS[136]
AF7	VSS[58]	VSS[137]
AF8	VSS[59]	VSS[138]
AG19	VSS[60]	VSS[139]
AG2	VSS[61]	VSS[140]
AG31	VSS[62]	VSS[141]
AG48	VSS[63]	VSS[142]
AH11	VSS[64]	VSS[143]
AH3	VSS[65]	VSS[144]
AH36	VSS[66]	VSS[145]
AH39	VSS[67]	VSS[146]
AH40	VSS[68]	VSS[147]
AH42	VSS[69]	VSS[148]
AH46	VSS[70]	VSS[149]
AH7	VSS[71]	VSS[150]
AJ19	VSS[72]	VSS[151]
AJ21	VSS[73]	VSS[152]
AJ24	VSS[74]	VSS[153]
AJ33	VSS[75]	VSS[154]
AJ34	VSS[76]	VSS[155]
AK12	VSS[77]	VSS[156]
AK3	VSS[78]	VSS[157]
	VSS[79]	VSS[158]

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AY4	VSS[159]	
AY42	VSS[160]	VSS[259]
AY46	VSS[161]	VSS[260]
AY8	VSS[162]	VSS[261]
B11	VSS[163]	VSS[262]
B15	VSS[164]	VSS[263]
B19	VSS[165]	VSS[264]
B23	VSS[166]	VSS[265]
B27	VSS[167]	VSS[266]
B31	VSS[168]	VSS[267]
B35	VSS[169]	VSS[268]
B39	VSS[170]	VSS[269]
B7	VSS[171]	VSS[270]
F45	VSS[172]	VSS[271]
BB12	VSS[173]	VSS[272]
BB16	VSS[174]	VSS[273]
BB20	VSS[175]	VSS[274]
BB22	VSS[176]	VSS[275]
BB24	VSS[177]	VSS[276]
BB28	VSS[178]	VSS[277]
BB30	VSS[179]	VSS[278]
BB38	VSS[180]	VSS[279]
BB4	VSS[181]	VSS[280]
BB46	VSS[182]	VSS[281]
BC14	VSS[183]	VSS[282]
BC18	VSS[184]	VSS[283]
BC2	VSS[185]	VSS[284]
BC22	VSS[186]	VSS[285]
BC26	VSS[187]	VSS[286]
BC32	VSS[188]	VSS[287]
BC34	VSS[189]	VSS[288]
BC36	VSS[190]	VSS[289]
BC40	VSS[191]	VSS[290]
BC42	VSS[192]	VSS[291]
BC48	VSS[193]	VSS[292]
BD46	VSS[194]	VSS[293]
BD5	VSS[195]	VSS[294]
BE22	VSS[196]	VSS[295]
BE26	VSS[197]	VSS[296]
BE40	VSS[198]	VSS[297]
BF10	VSS[199]	VSS[298]
BF12	VSS[200]	VSS[299]
BF16	VSS[201]	VSS[300]
BF20	VSS[202]	VSS[301]
BF22	VSS[203]	VSS[302]
BF24	VSS[204]	VSS[303]
BF26	VSS[205]	VSS[304]
BF28	VSS[206]	VSS[305]
BD3	VSS[207]	VSS[306]
BD30	VSS[208]	VSS[307]
BF38	VSS[209]	VSS[308]
BF40	VSS[210]	VSS[309]
BF8	VSS[211]	VSS[310]
BG17	VSS[212]	VSS[311]
BG21	VSS[213]	VSS[312]
BG33	VSS[214]	VSS[313]
BG44	VSS[215]	VSS[314]
BG8	VSS[216]	VSS[315]
BH11	VSS[217]	VSS[316]
BH15	VSS[218]	VSS[317]
BH17	VSS[219]	VSS[318]
BH19	VSS[220]	VSS[319]
H10	VSS[221]	VSS[320]
AV30	VSS[222]	VSS[321]
BH27	VSS[223]	VSS[322]
BH31	VSS[224]	VSS[323]
AV4	VSS[225]	VSS[324]
BH33	VSS[226]	VSS[325]
BH35	VSS[227]	VSS[326]
BH39	VSS[228]	VSS[327]
BH43	VSS[229]	VSS[328]
AW18	VSS[230]	VSS[329]
D3	VSS[231]	VSS[330]
D12	VSS[232]	VSS[331]
D16	VSS[233]	VSS[332]
D18	VSS[234]	VSS[333]
D22	VSS[235]	VSS[334]
D24	VSS[236]	VSS[335]
D26	VSS[237]	VSS[336]
D30	VSS[238]	VSS[337]
D32	VSS[239]	VSS[338]
D34	VSS[240]	VSS[339]
D38	VSS[241]	VSS[340]
D42	VSS[242]	VSS[341]
D8	VSS[243]	VSS[342]
E18	VSS[244]	VSS[343]
E26	VSS[245]	VSS[344]
G18	VSS[246]	VSS[345]
G20	VSS[247]	VSS[346]
G26	VSS[248]	VSS[347]
G28	VSS[249]	VSS[348]
G36	VSS[250]	VSS[349]
G48	VSS[251]	VSS[350]
H12	VSS[252]	VSS[351]
H18	VSS[253]	VSS[352]
H22	VSS[254]	
H24	VSS[255]	
H26	VSS[256]	
H30	VSS[257]	
H32	VSS[258]	
H34		
F3		

CPT_PPT_Rev_0p5

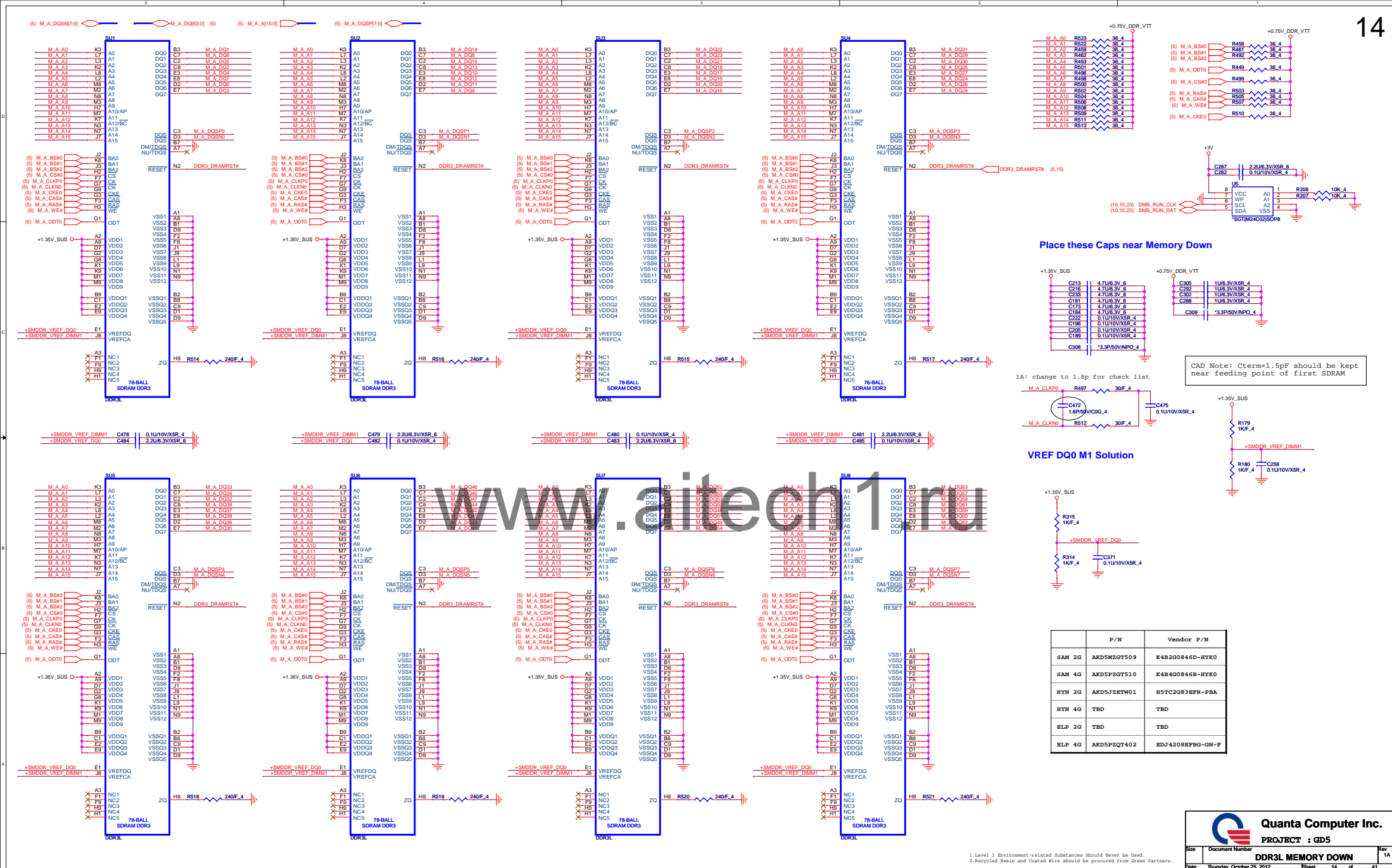
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K18	
K26	
K39	
K46	
K7	
L18	
L2	
L20	
L26	
L28	
L36	
L48	
M12	
P16	
M18	
M22	
M24	
M30	
M32	
M34	
M38	
M4	
M42	
M46	
M8	
N18	
P30	
N47	
P11	
P18	
T33	
P40	
P43	
P47	
P7	
R48	
T12	
T31	
T37	
T4	
W34	
T46	
T47	
T8	
V11	
V17	
V26	
V27	
V29	
V31	
V36	
V38	
V43	
V7	
W10	
W19	
W2	
W27	
W48	
Y12	
Y38	
Y4	
Y42	
Y46	
Y8	
BG29	
N24	
AJ3	
AD47	
B43	
BE10	
BG41	
G14	
H16	
T36	
BG22	
BG24	
C22	
AP13	
M14	
AP3	
AP1	
BE16	
BC16	
BG28	
BJ28	



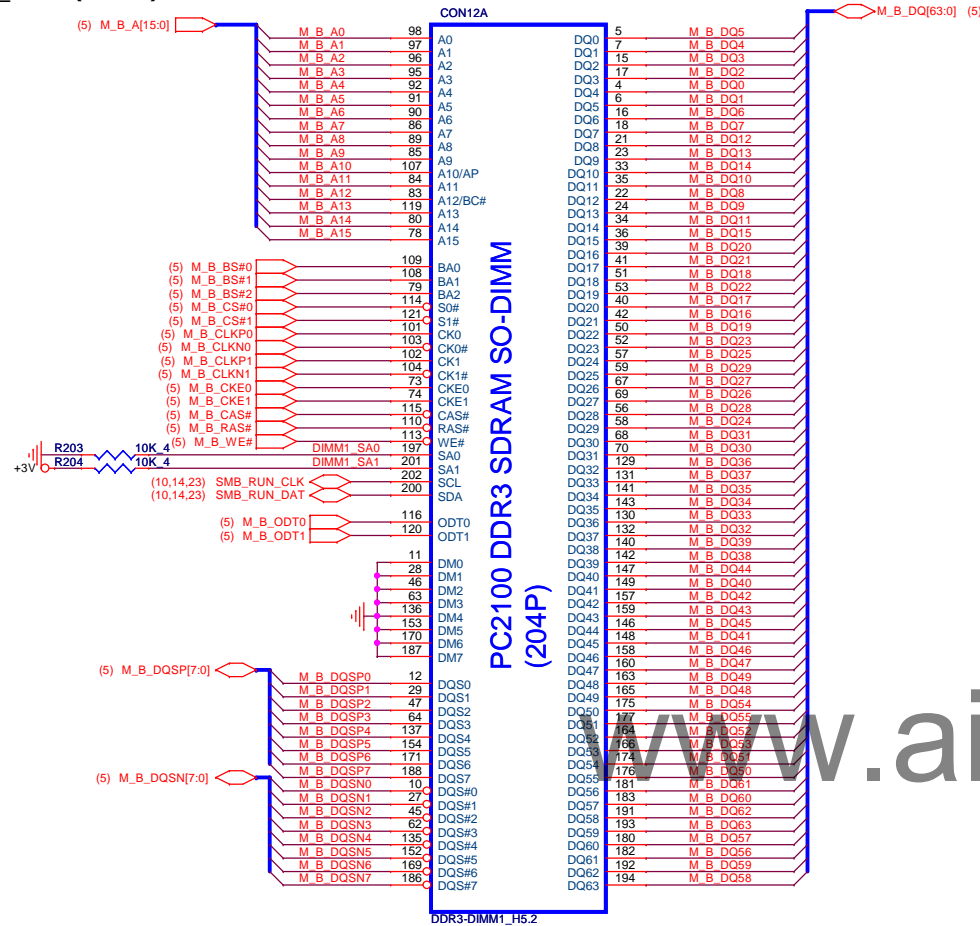
Quanta Computer Inc.

PROJECT : GD5

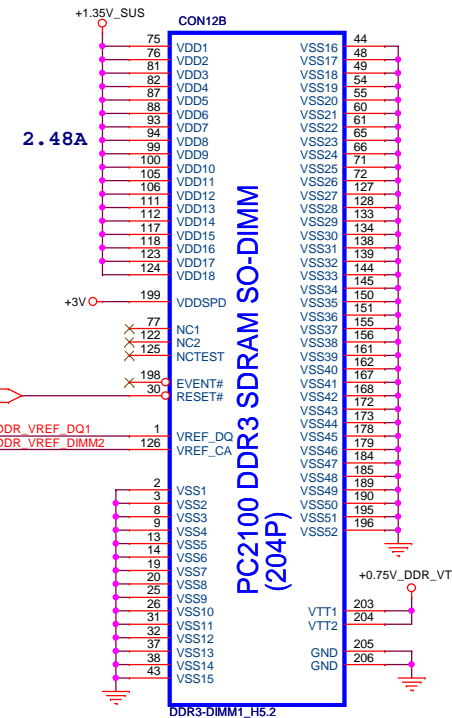
Size	Document Number	Rev
		1A
CPT/PPT 6/6		
Date:	Thursday, October 25, 2012	Sheet 13 of 41



DDR_RVS (DDR)

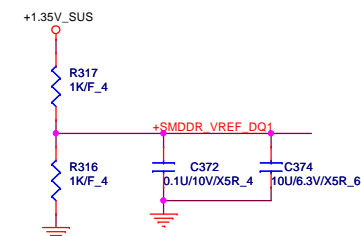


DDR3-DIMM1_H5.2
RUV Type

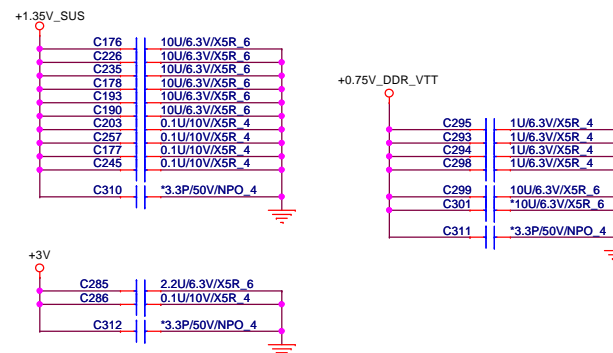


RUV Type

VREF DQ1 M1 Solution



Place these Caps near So-Dimm1.

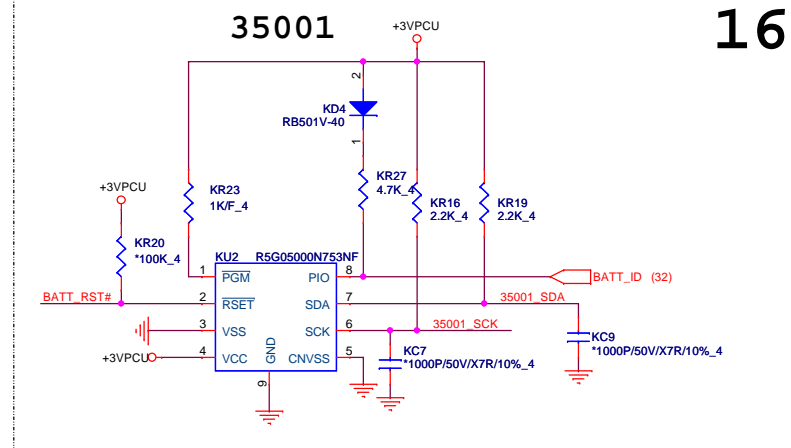
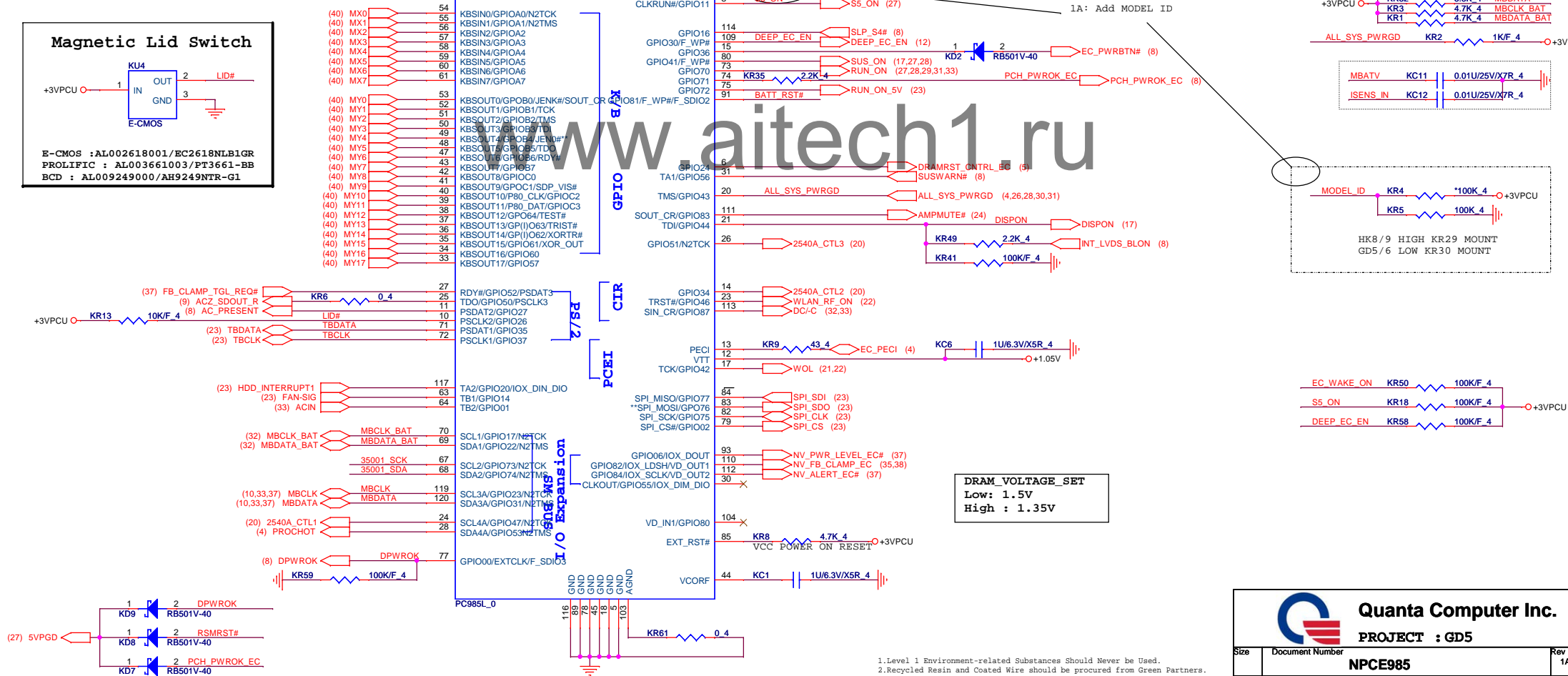
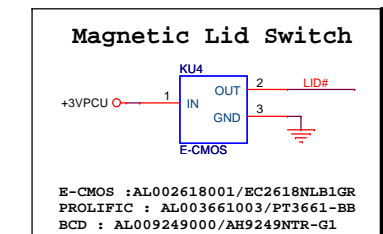


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PROJECT : GD5

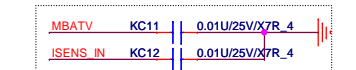
Size	Document Number	Rev
	DDRIII SO-DIMM-1	1A
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1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Since ECSCI is OD, no need for a back-drive protection diode on this signal. But note there is internal PU in chipset at default



TBCLK KR51 4.7K 4 +3V
TBDATA KR52 4.7K 4 +3V



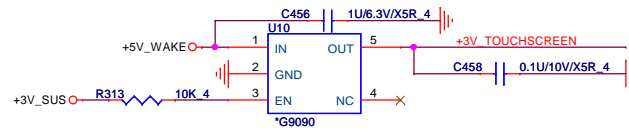
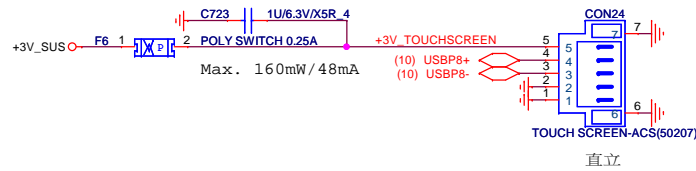
HK8/9 HIGH KR29 MOUNT
GD5/6 LOW KR30 MOUNT

```

DRAM_VOLTAGE_SET
Low: 1.5V
High : 1.35V

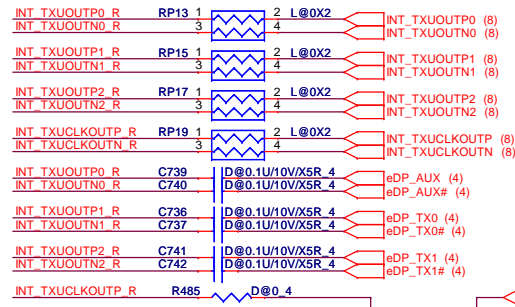
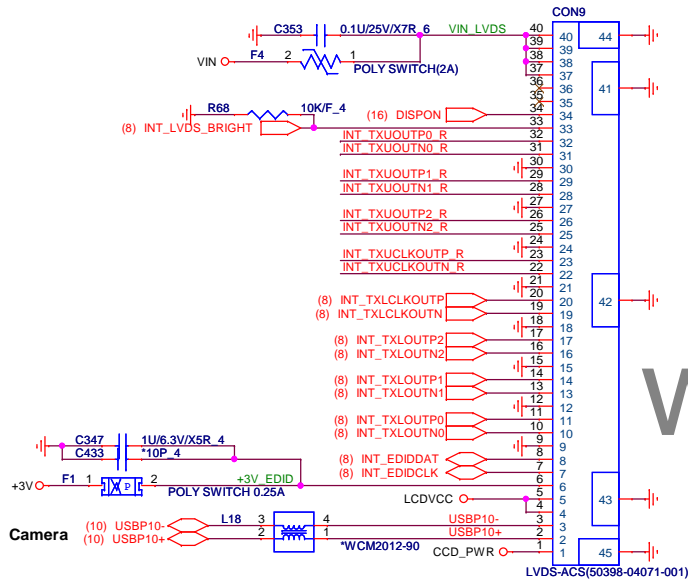
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Touch Screen

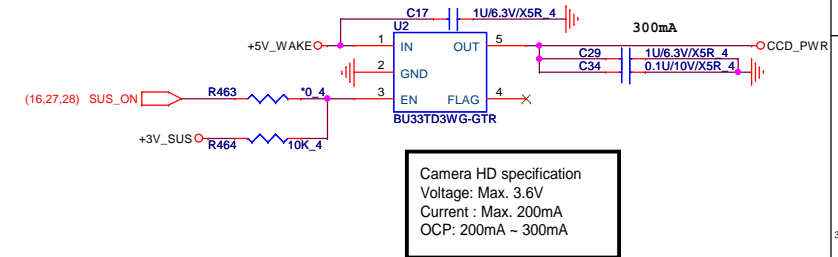


LVDS

FAST, UL/CSA

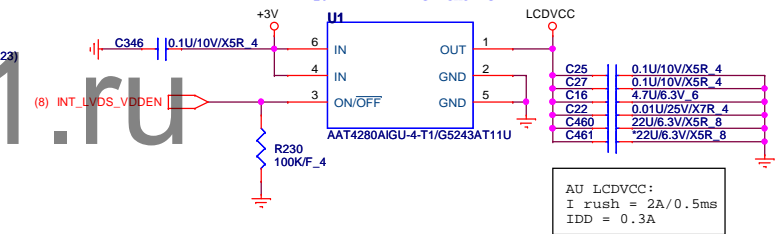


USB Camera Power



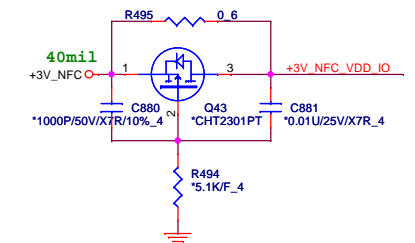
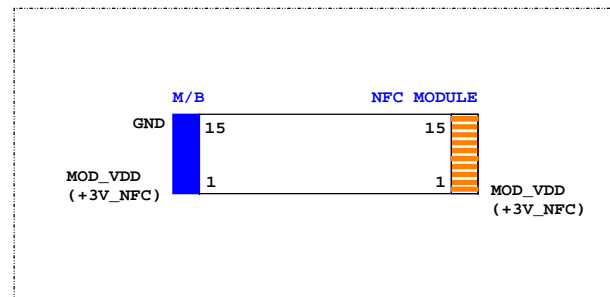
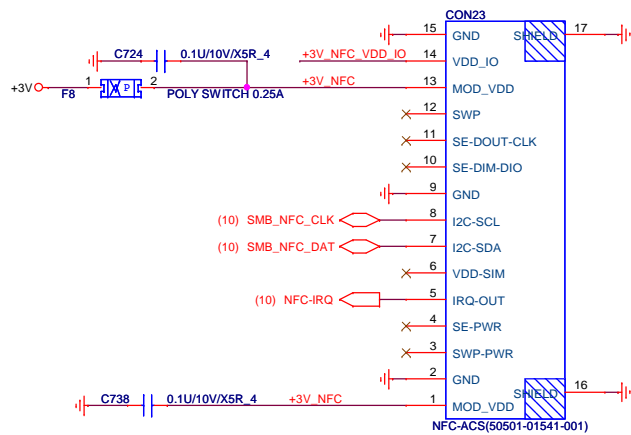
Camera HD specification
Voltage: Max. 3.6V
Current : Max. 200mA
OCP: 200mA ~ 300mA

NB LVDS enable

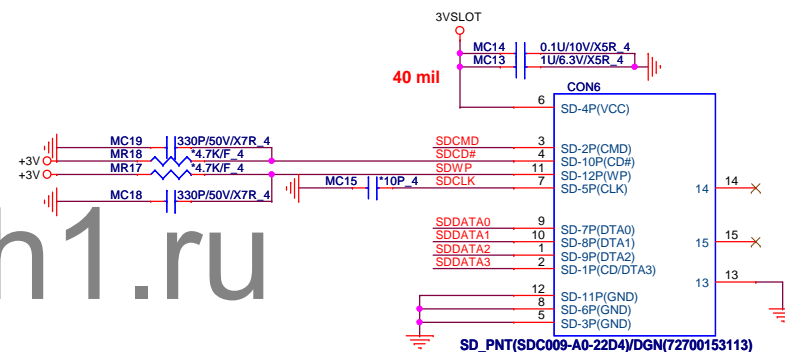
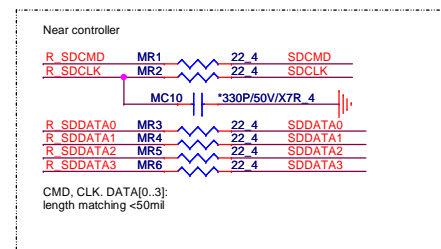
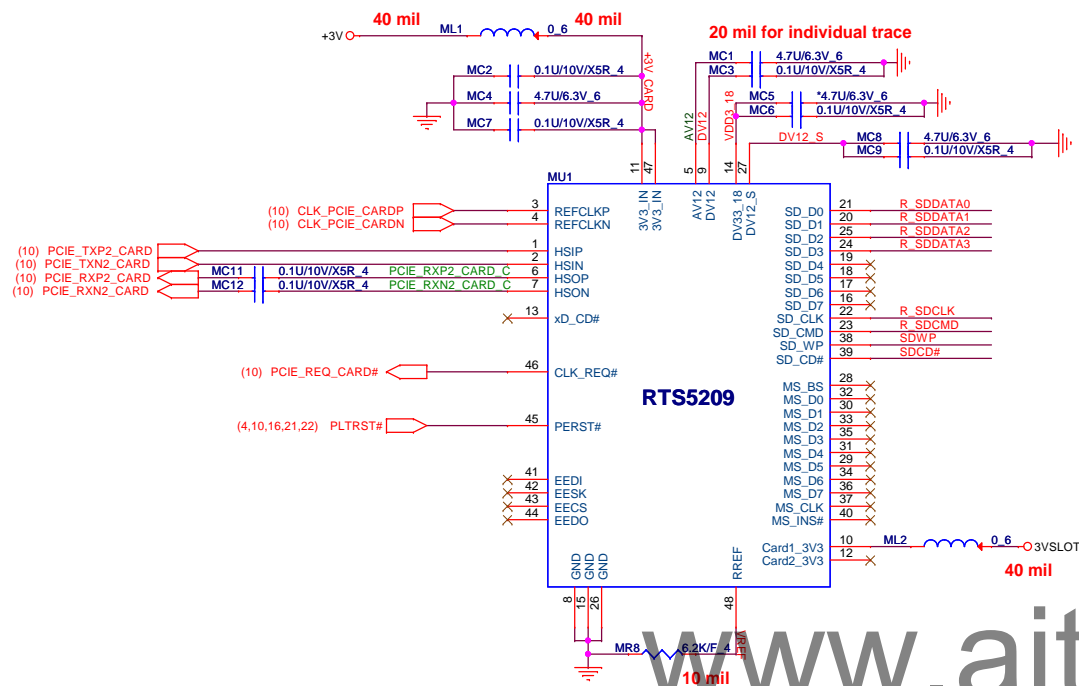


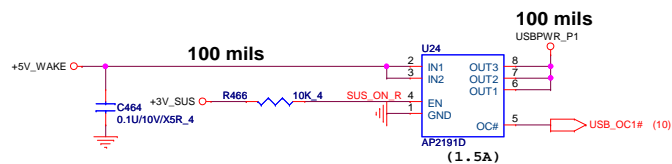
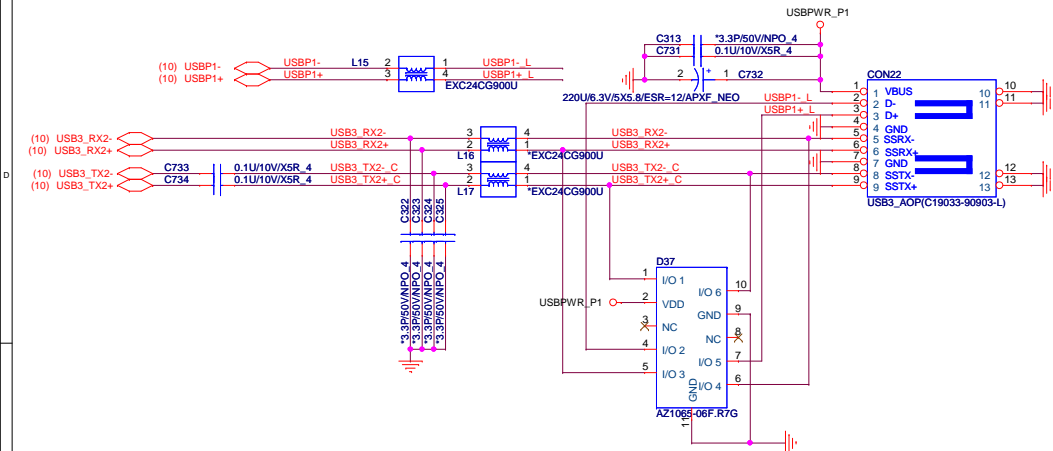
AU LCDVCC:
I rush = 2A/0.5ms
IDD = 0.3A

NFC

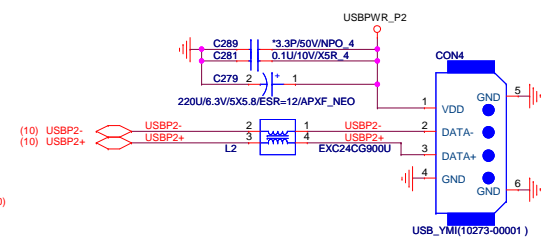
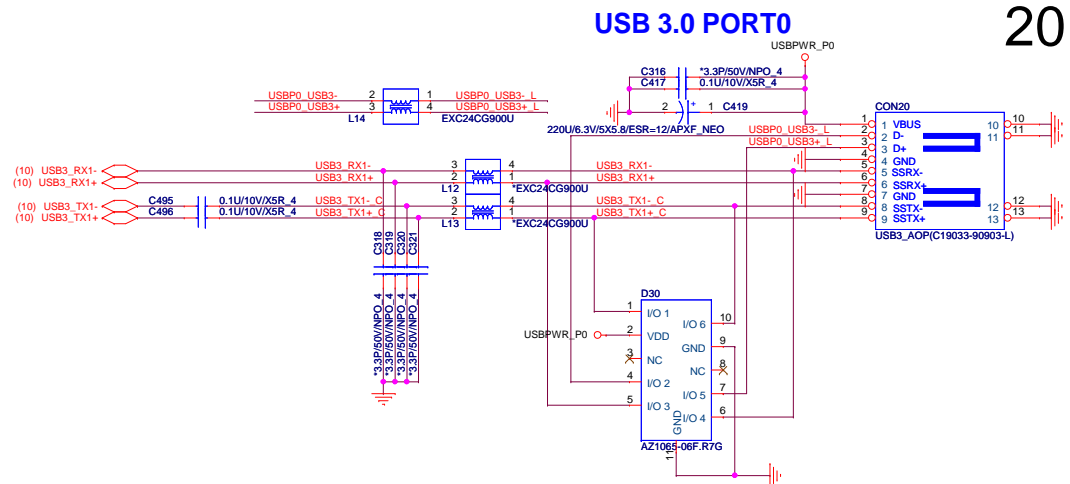
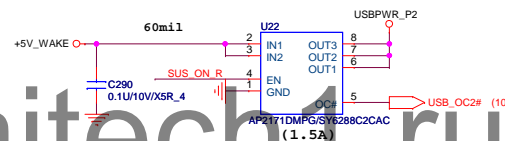


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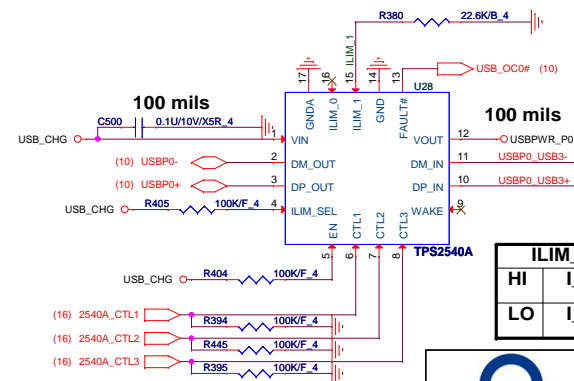


USB 2.0(GD6 Only)



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USB Charger



ILIM_SEL (I LIMIT(A)= 48000/R)		
HI	I_LIM_1	
LO	I_LIM_0	48000/22.6K=2.123A

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PROJECT : GD5

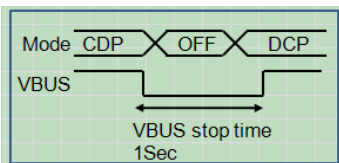
Size Document Number Rev 1A

USB/USB Charger

Date: Thursday, October 25, 2012 Sheet 20 of 41

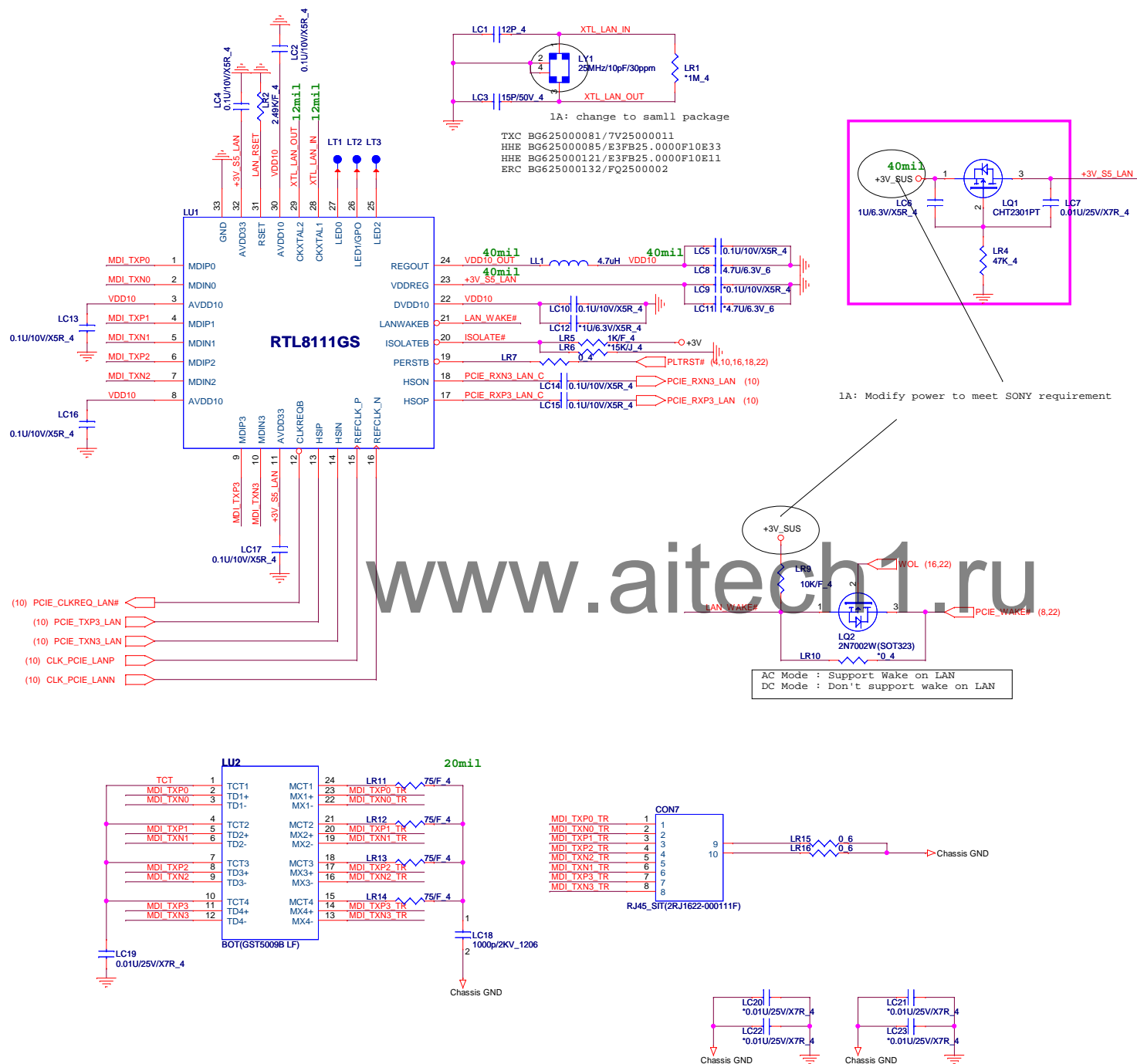
1. Level 1 Environment-related Substances Should Never be Used.
2. Recycled Resin and Coated Wire should be procured from Green Partners.

SDP : Standard Downstream Port
CDP : Charging downstream port
DCP : Dedicated Charging Port
Enable/Disable : setting by BIOS

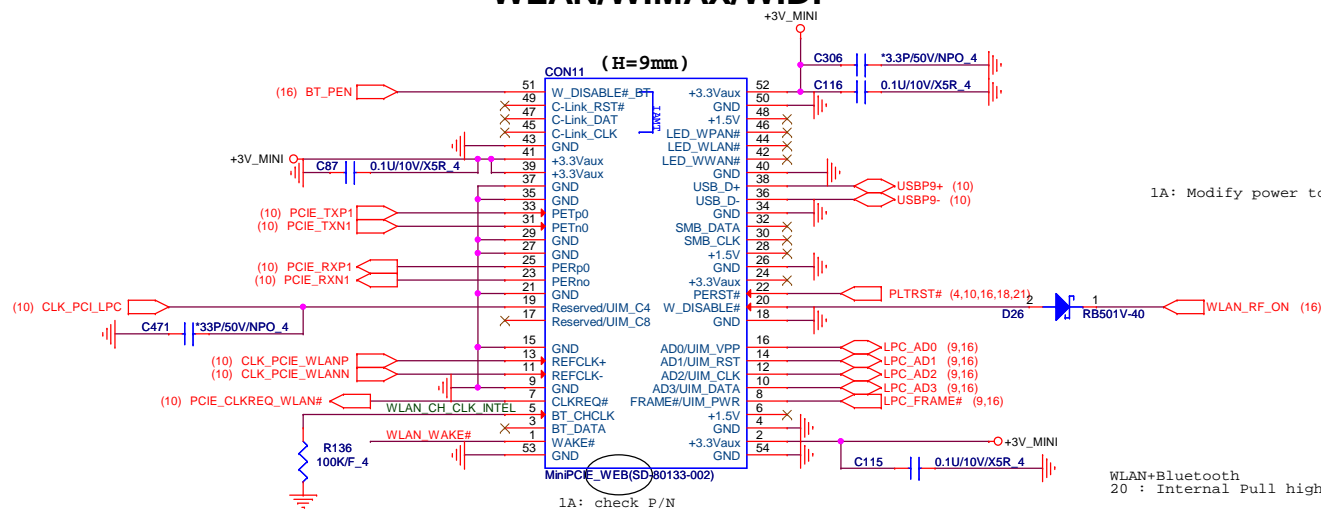


CTL_1	CTL_2	CTL_3	TPS 2540A/2543 Truth Table
0	0	0	OUT discharge, power switch OFF
0	X	1	DCP, Auto-detect (S3/S4/S5, 1.5A)
X	1	0	SDP, USB2.0 mode (S0, 0.5A)
1	0	0	DCP, BC SPEC1.2 only (S3/Deep standby/S4/S5, 1.5A)
1	0	1	DCP, Divider mode only (S3/S4/S5, 1.5A)
1	1	1	CDP (S0, 1.5A)

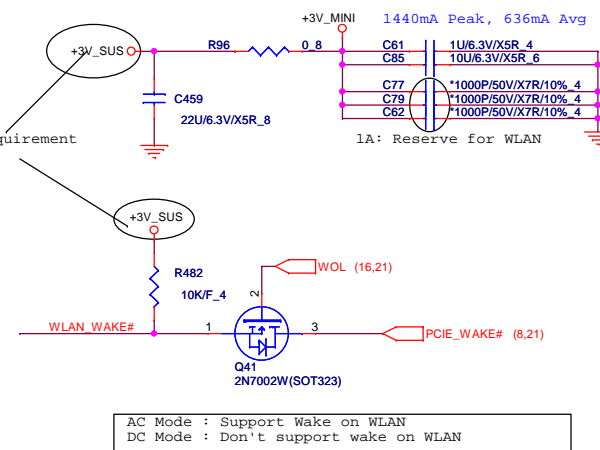
System State	USB Battery Charging Setting			
	Disable	C(1 2 3)	Enable	C(1 2 3)
S0	SDP	(X 1 0)	CDP	(1 1 1)
S3	SDP	(X 1 0)	DCP BC	(1 0 0)
DS3	Charger OFF	(0 0 0)	DCP BC	(1 0 0)
S4	Charger OFF	(0 0 0)	DCP BC	(1 0 0)
S5	Charger OFF	(0 0 0)	DCP BC	(1 0 0)



WLAN/WIMAX/WIDI



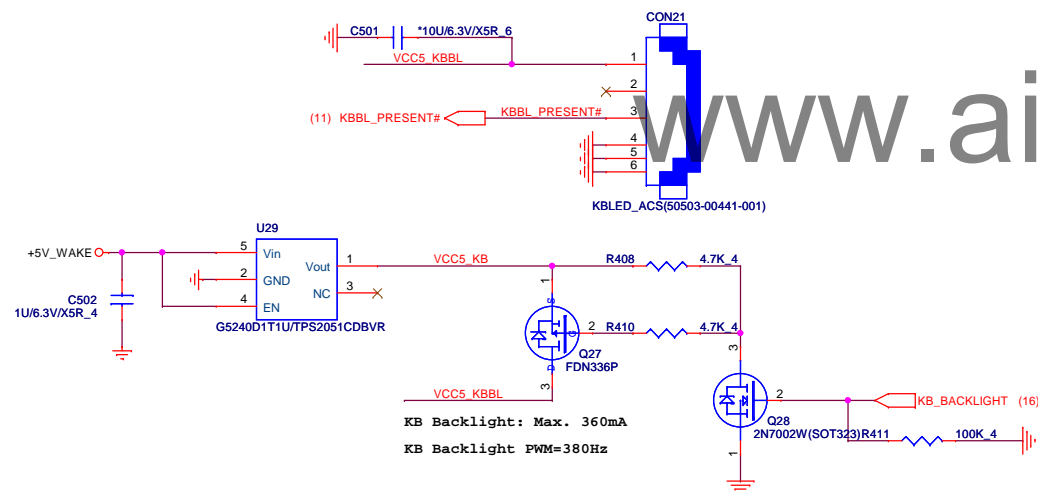
1A: Modify power to meet SONY requirement

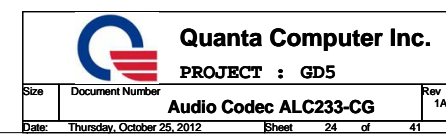


WLAN+Bluetooth
20 : Internal Pull high 25K ~ 58K

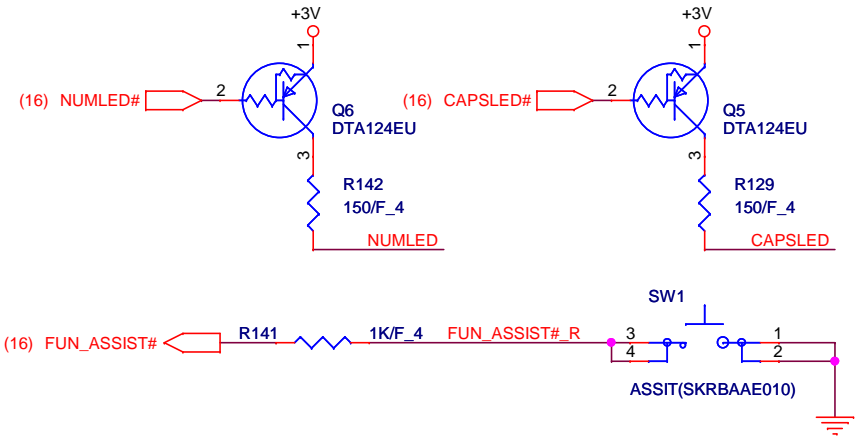
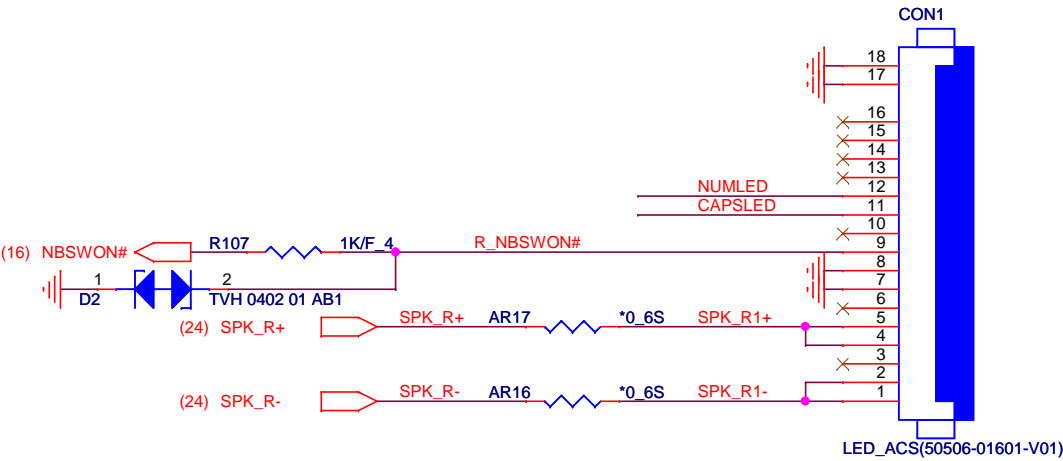
AC Mode : Support Wake on WLAN
DC Mode : Don't support wake on WLAN

KB BACKLIGHT

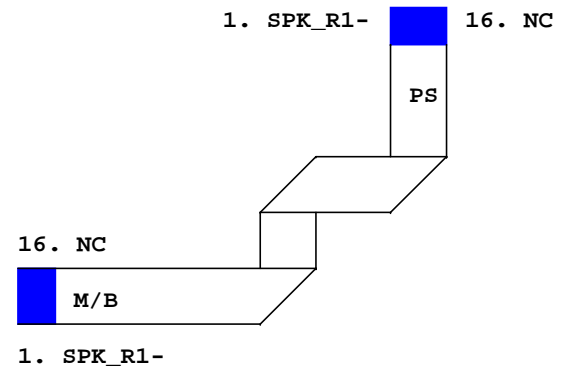
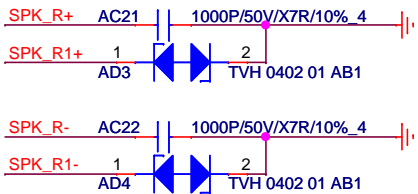
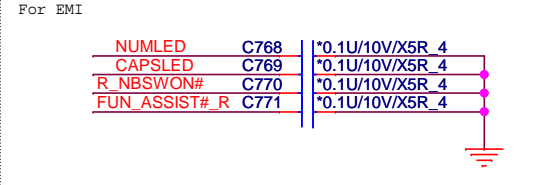




Power SW Board Connector



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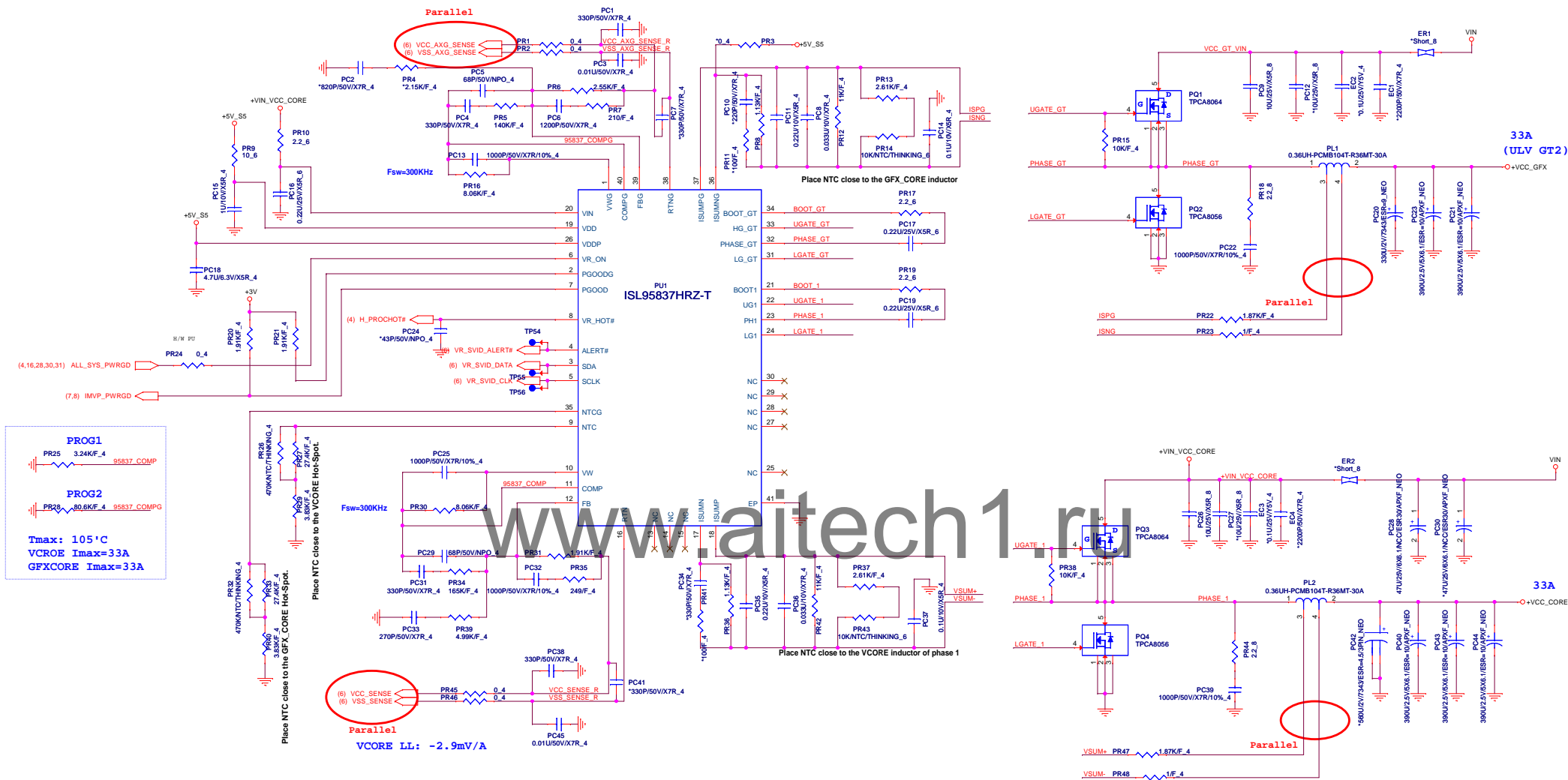


1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

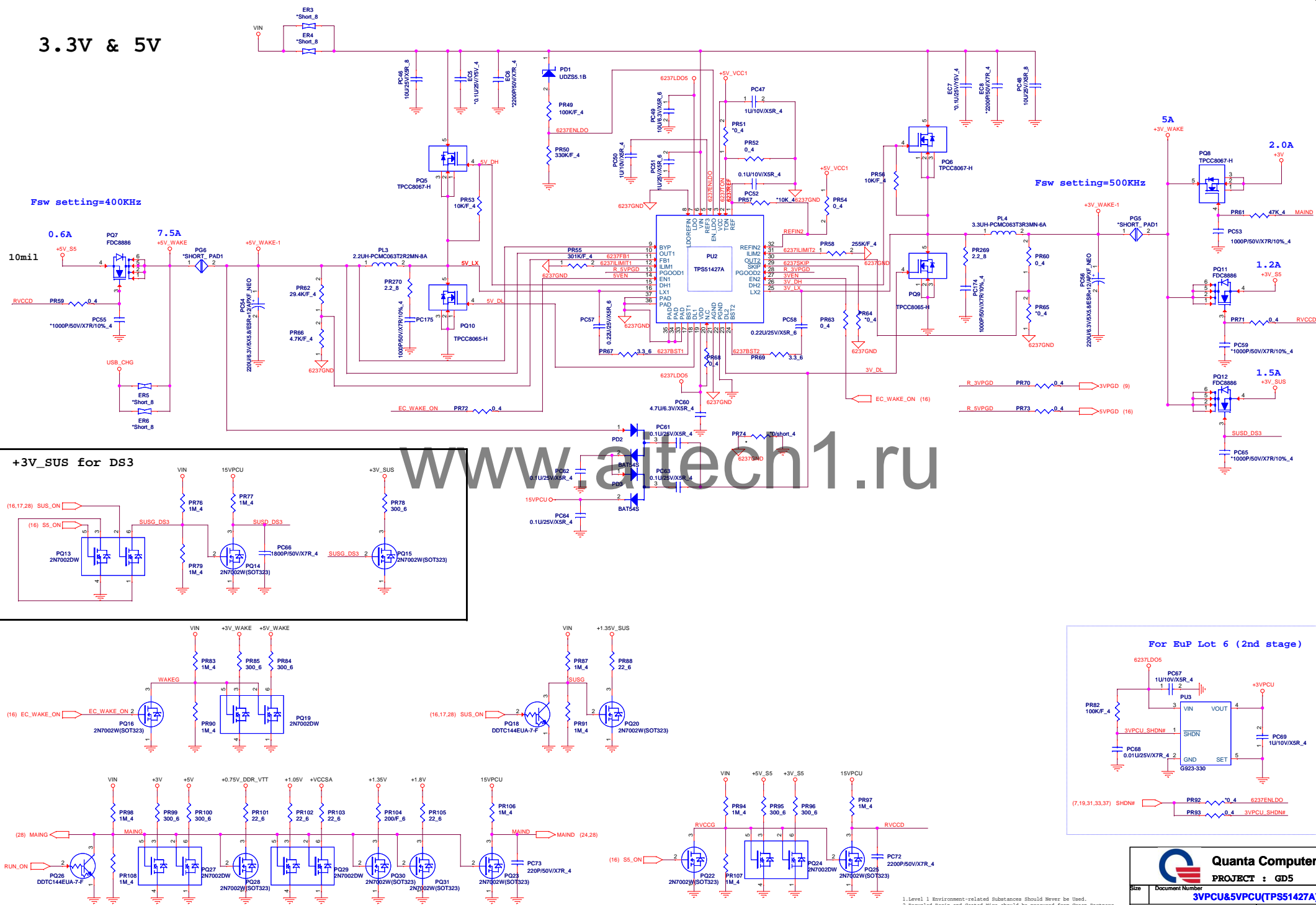
Quanta Computer Inc.
PROJECT :GD5

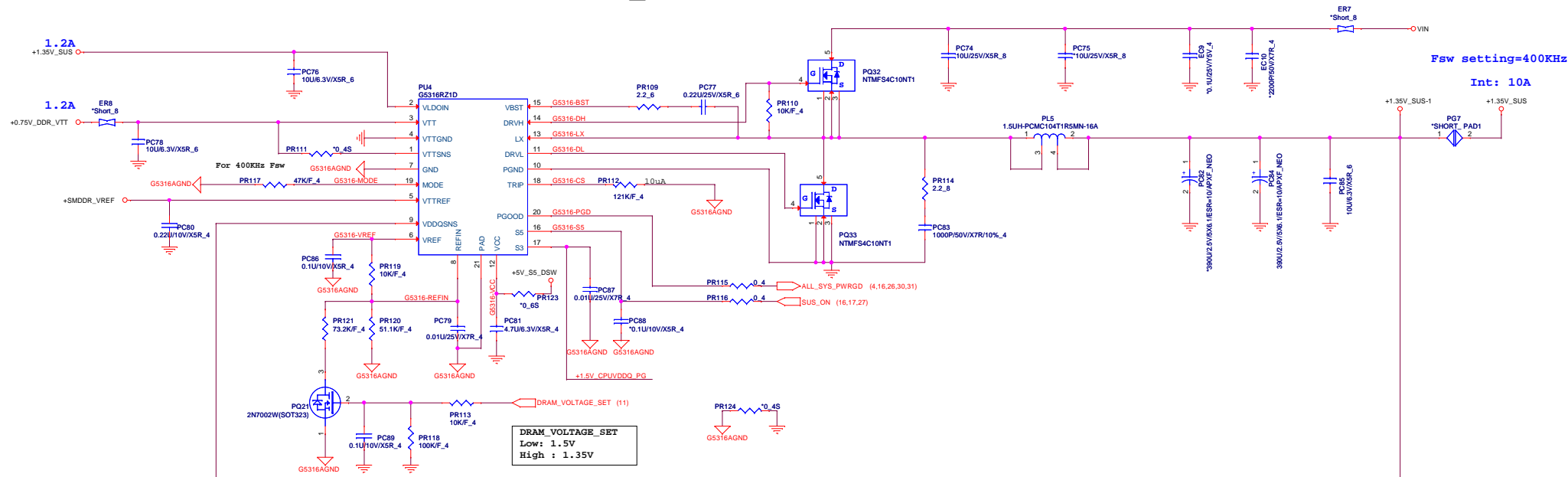
Size	Document Number	Rev
	LED/PS	1A
Date:	Thursday, October 25, 2012	Sheet 25 of 41

GFX_CORE LL: -3.9mV/A for GT2

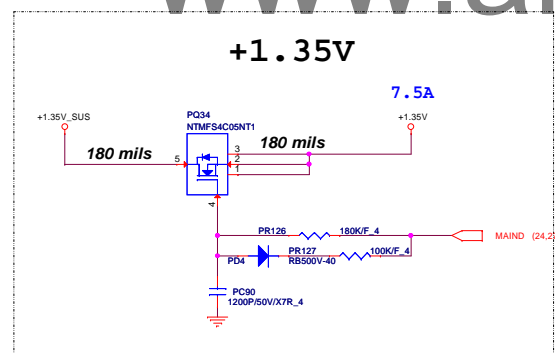


3.3V & 5V

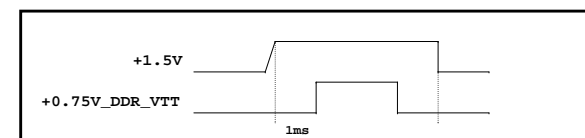
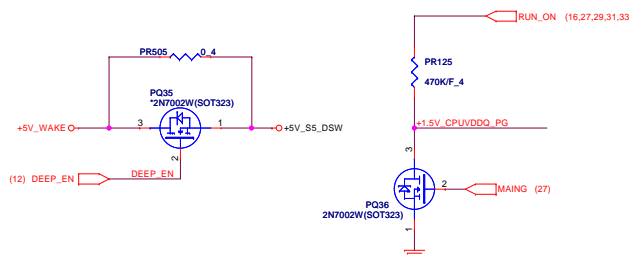


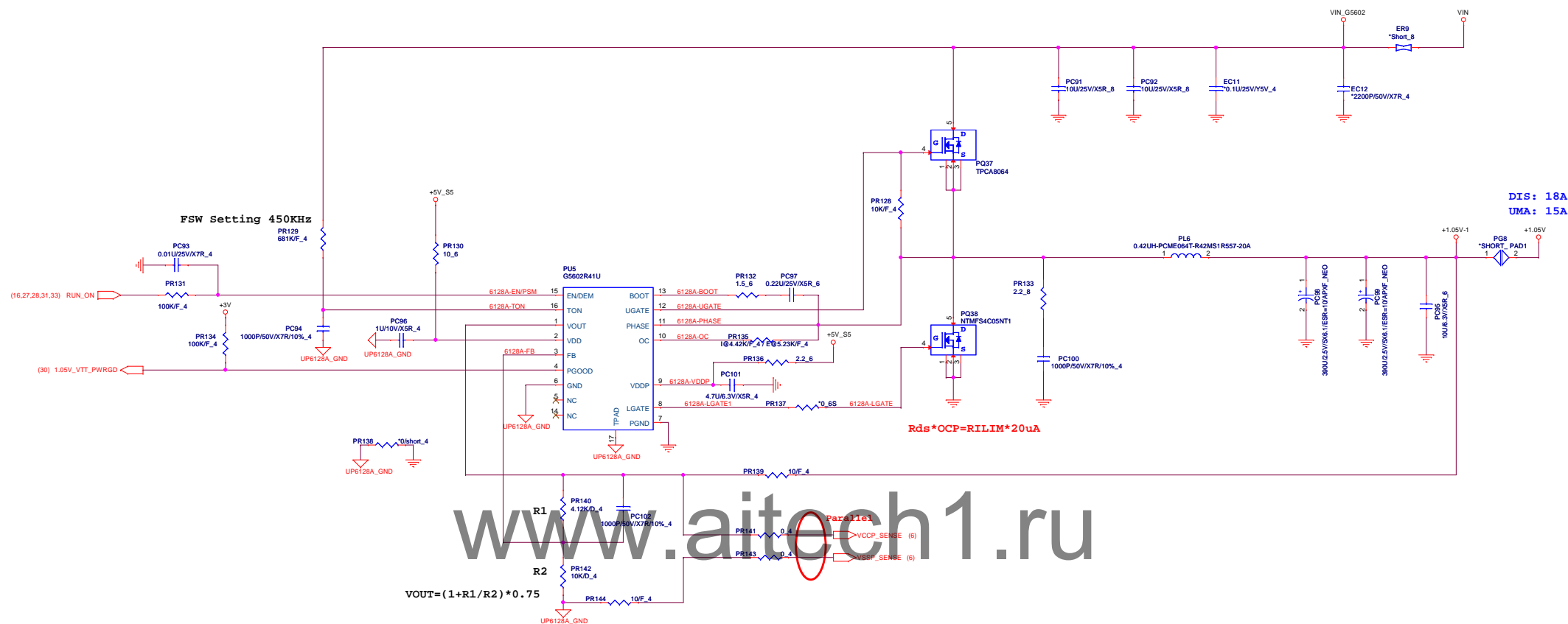


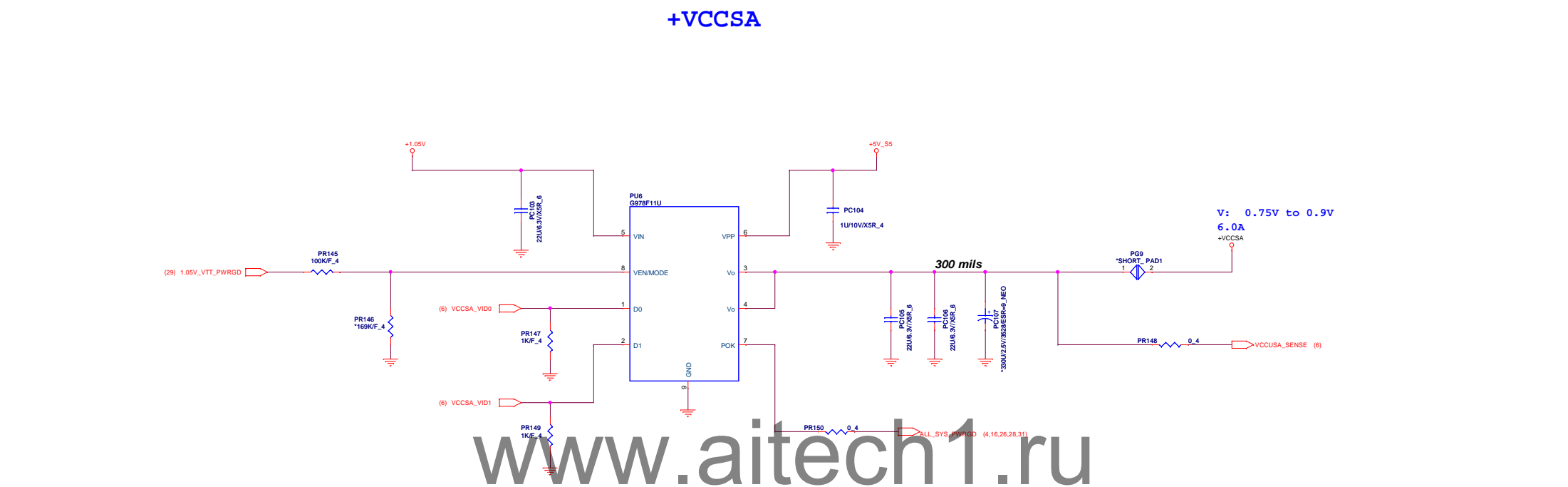
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STATE	S3	S5	1.5VSUS	VTTREF	VTT
S0	1	1	On	On	On
S3	0	1	On	On	Off/High Z
S4/S5	0	0	Off	Off	Off





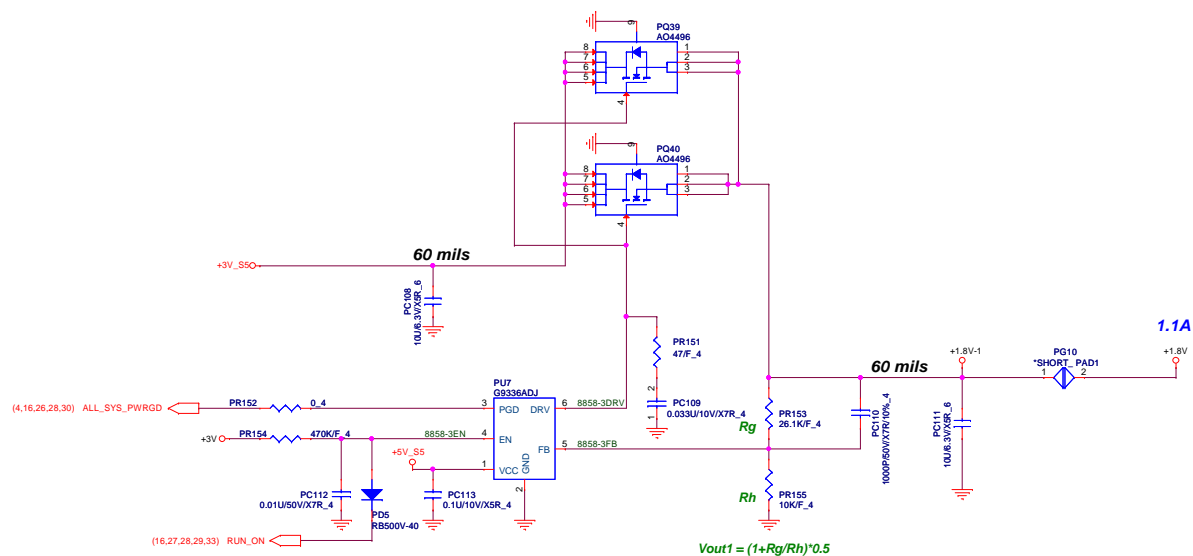


For Chief River ULV

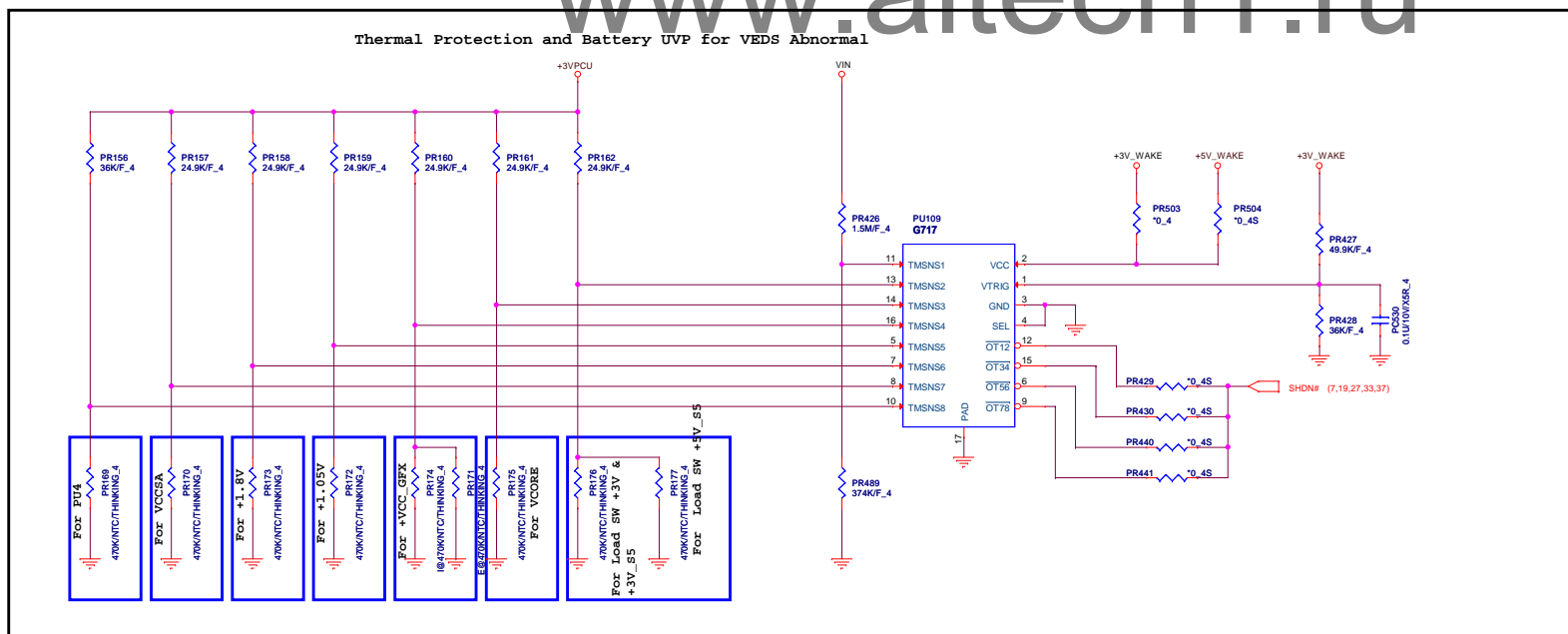
VCCSA_VID0	VCCSA_VID1	+VCCSA
0	0	0.9V
0	1	0.85V
1	0	0.775V
1	1	0.75V

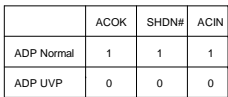
1. Level 1 Environment-related Substances Should Never be Used.
2. Recycled Resin and Coated Wire should be procured from Green Partners.

VCC1.8

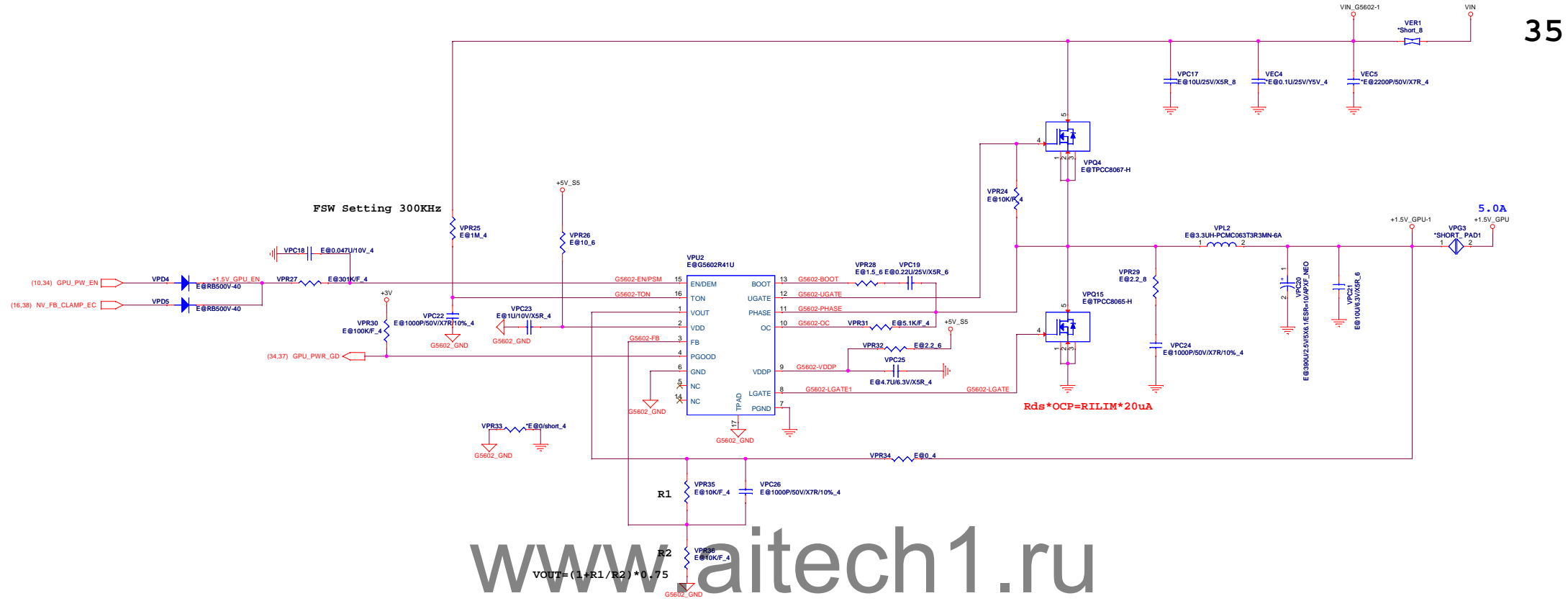


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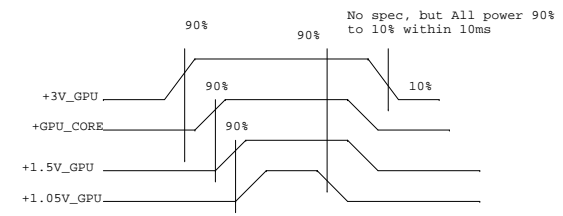
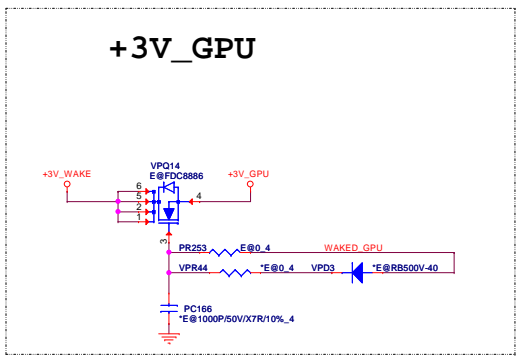
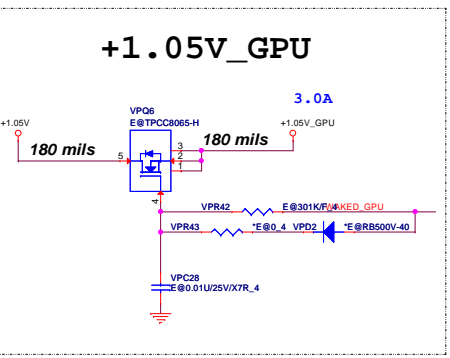
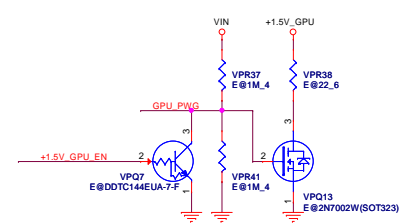
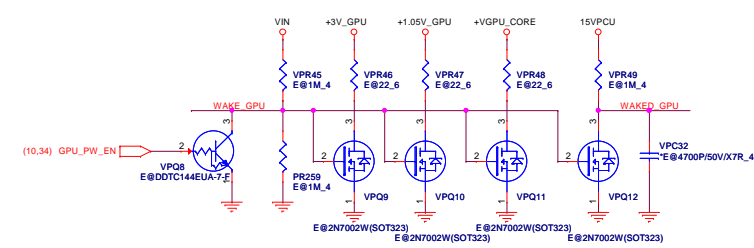






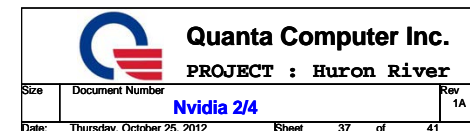


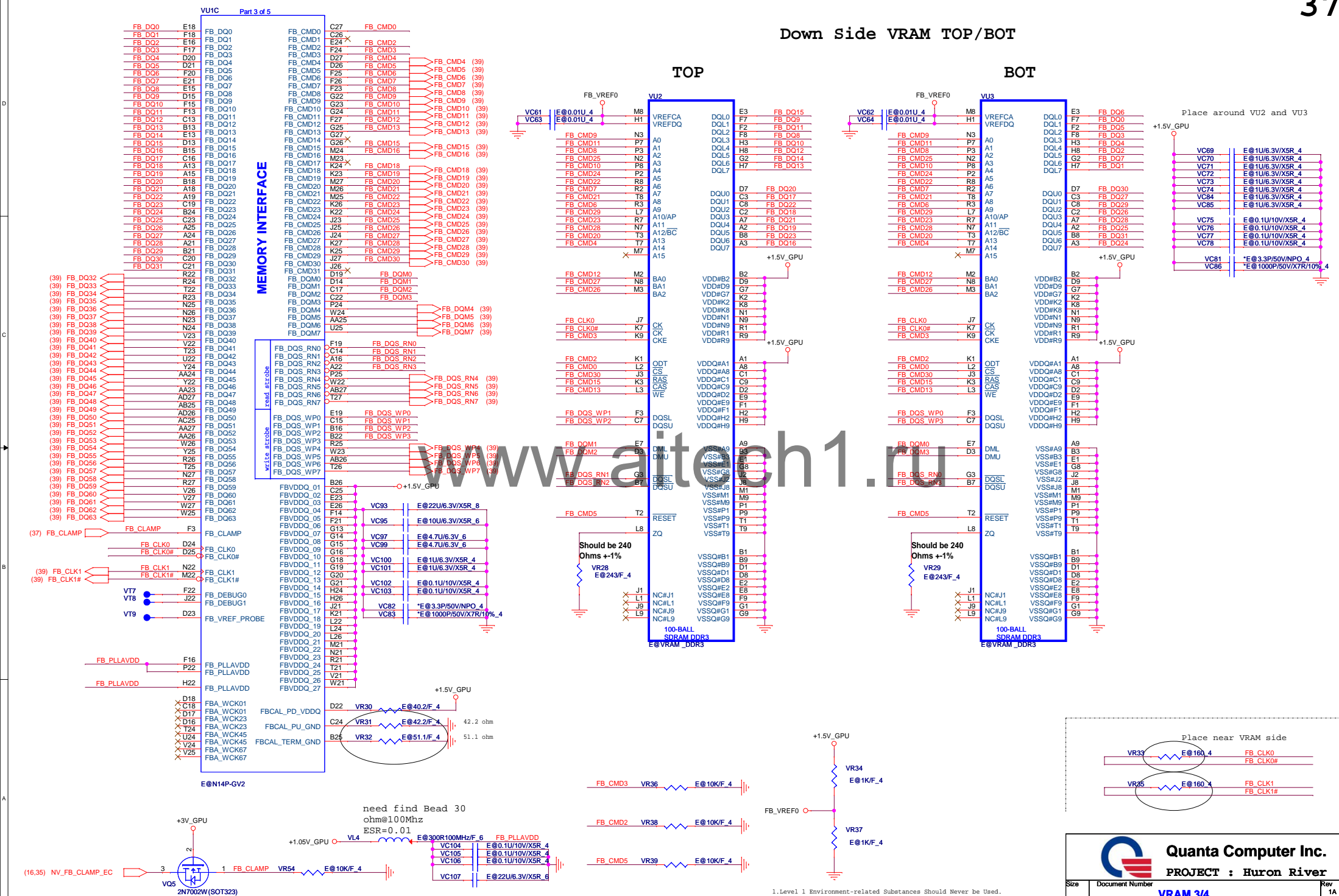
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1. Level 1 Environment-related Substances Should Never be Used.
 2. Recycled Resin and Coated Wire should be procured from Green Partners.



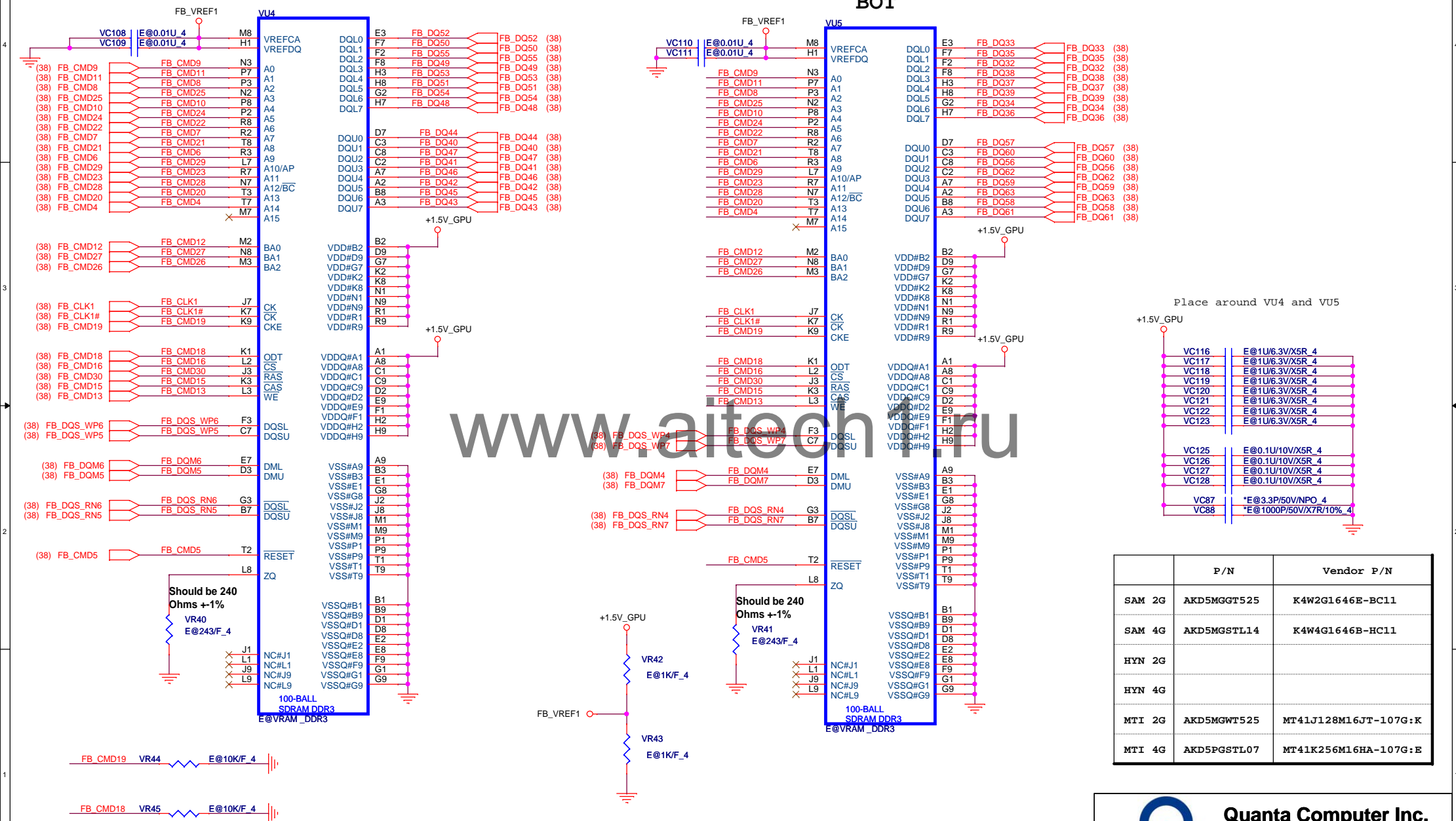




Up Side VRAM TOP/BOT

TOP

BOT



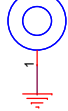
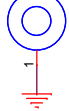
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PROJECT : Huron River

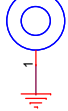
Size	Document Number	Rev
	VRAM 4/4	1A
Date:	Thursday, October 25, 2012	Sheet 39 of 41

1.Level 1 Environment-related Substances Should Never be Used.
 2.Recycled Resin and Coated Wire should be procured from Green Partners.

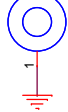
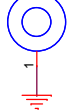
GRAPHIC NUT

H4
H-C217D118P2H5
H-C217D118P2

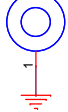
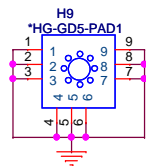
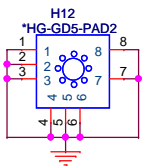
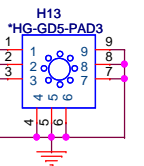
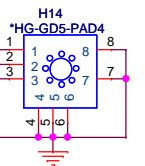
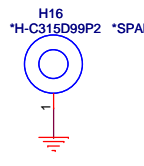
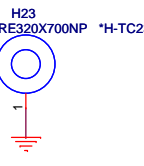
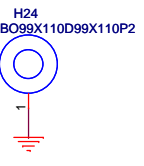
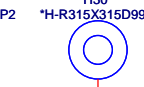
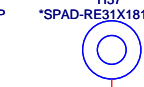
PCH NUT

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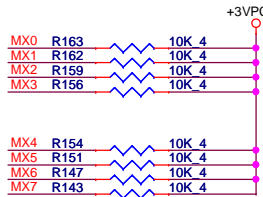
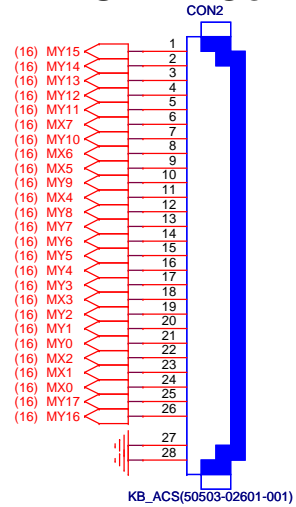
WALN NUT

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h-tc217bc182d142p2

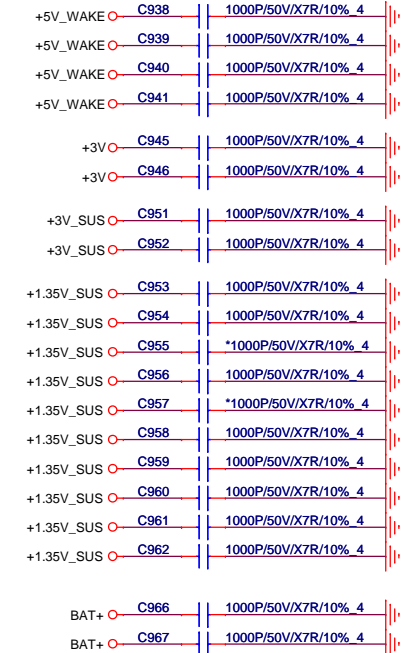
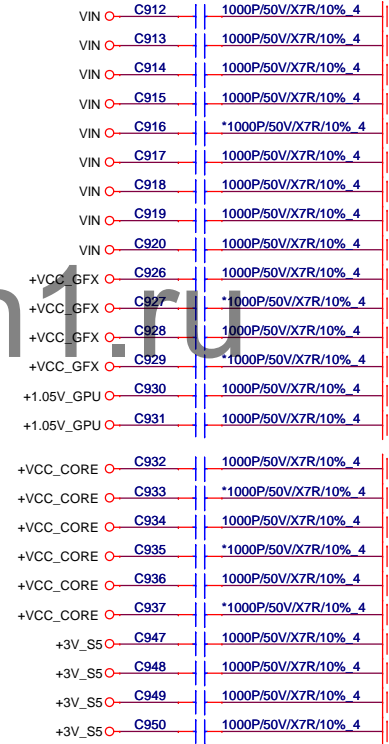
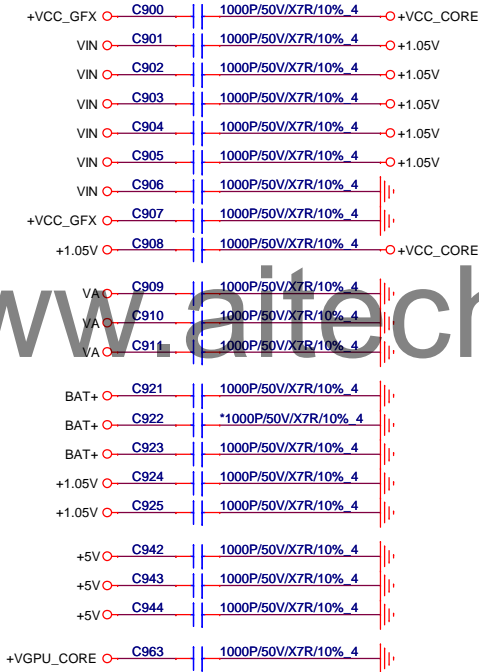
HDD/B NUT

H7
h-c217d146p2H3
*H-GD5-PAD6H9
*HG-GD5-PAD1H10
*HG-GD5-PAD5H11
*H-R315I139D99P2H12
*HG-GD5-PAD2H13
*HG-GD5-PAD3H14
*HG-GD5-PAD4H16
*H-C315D99P2H23
*SPAD-RE320X700NPH24
*H-TC236BO99X110D99X110P2H25
*H-C236D99X110P2H27
*H-R315X315D99P2H28
*H-R315X315D99P2H29
*H-R315X315D99P2H30
*H-R315X315D99P2H31
*H-C236D99P2H32
*H-C276D276NH33
*H-C276D276NH34
*PAD-C177H35
*PAD-C177H36
*SPAD-RE31X181NPH37
*SPAD-RE31X181NP

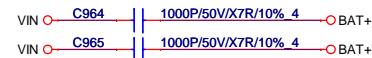
KEY BOARD Connector



MY15	C744	*220P 4
MY14	C745	*220P 4
MY13	C746	*220P 4
MY12	C747	*220P 4
MY11	C748	*220P 4
MX7	C749	*220P 4
MY10	C750	*220P 4
MX6	C751	*220P 4
MX5	C752	*220P 4
MY9	C753	*220P 4
MX4	C754	*220P 4
MY8	C755	*220P 4
MY7	C756	*220P 4
MY6	C757	*220P 4
MY5	C758	*220P 4
MY4	C759	*220P 4
MY3	C760	*220P 4
MX3	C761	*220P 4
MY2	C762	*220P 4
MY1	C763	*220P 4
MY0	C764	*220P 4
MX2	C765	*220P 4
MX1	C766	*220P 4
MX0	C767	*220P 4



	Quanta P/N	Vendor P/N	Foot Print
14 "	DFFC24FS002	88483-2441-FN	88483-2441-fn-24p-rdv_ab
15 "	DFFC26FR039	50503-02601-001	88513-2641-26p-l-smt



- Level 1 Environment-related Substances Should Never be Used.
- Recycled Resin and Coated Wire should be procured from Green Partners.



Quanta Computer Inc.
PROJECT : GD5

Size	Document Number	Rev
	HOLE/EMI/KB	1A

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USB PORT Architecture	
PORT 0	USB3.0
PORT 1	USN2.0
PORT 2	USN2.0
PORT 3	USB2.0
PORT 4	N/A
PORT 5	N/A
PORT 6	N/A
PORT 7	N/A
PORT 8	N/A
PORT 9	WiMax/BT
PORT 10	Camera
PORT 11	N/A
PORT 12	N/A
PORT 13	N/A

PCIE BUS	
PORT 1	WLAN Port
PORT 2	CARD READER
PORT 3	GLAN(RTL8111E)
PORT 4	N/A
PORT 5	N/A
PORT 6	N/A
PORT 7	N/A
PORT 8	N/A

SATA BUS	
PORT 0	HDD
PORT 1	N/A
PORT 2	N/A
PORT 3	N/A
PORT 4	ODD
PORT 5	N/A

SM BUS	MBCLK/MBDATA	WRITE	READ	Function
ISL88731CHRTZ	0001 001X	0001 0010	0001 0011	Charger
AMD Thames	0100 0001	-	0100 0001	Graphice
LIS331DL	0011 101X	0011 1010	0011 1011	G Sensor

SM BUS	MBCLK_BAT/MBDATA_BAT	WRITE	READ	Function
VGP-BPS26	0001 011X	0001 0110	0001 0111	Battery

SM BUS	SMB_PCH_CLK/SMB_PCH_DAT	WRITE	READ	Function
DIMM Module0	1010 000X	1010 0000	1010 0001	DDRIII
DIMM Module 1	1010 010X	1010 0100	1010 0101	DDRIII
Synaptics	0010 110X	0010 1100	0010 1101	Click PAD

	R363(High) R362(Low)	R294(High) R297(low)
	Board ID3	Board ID0
14"/HK6	0	0
15"/HK5	0	1
17"/HK7	1	0

Board ID1 (VRAM Vendor)	Samaung(1)	Hynix(0)
R47(High)	Stuff	No Stuff
R48(Low)	No Stuff	Stuff

Board ID2		
14" 4PCS	1G	512M
15" 8PCS	1G	2G
R39(High)	Stuff	No Stuff
R27(Low)	No Stuff	Stuff

PCBA SKU	Discrete	UMA
R277(Pull High)	Stuff	No Stuff
R275(Pull Low)	No Stuff	Stuff

	S0	S3	DS3	S4	S5 (Charger Enable)	S5 (Charger Disable)
RUN_ON	H	L	L	L	L	L
+3V	H	L	L	L	L	L
+5V	H	L	L	L	L	L
+0.75V_DDR_VTT	H	L	L	L	L	L
+1.05V	H	L	L	L	L	L
+0.85V	H	L	L	L	L	L
+1.5V	H	L	L	L	L	L
+1.8V	H	L	L	L	L	L
+1.8V_GPU	H	L	L	L	L	L
+1.0V_GPU	H	L	L	L	L	L
+VGPU_CORE	H	L	L	L	L	L
+VCC_GFX	H	L	L	L	L	L
+VCC_CORE	H	L	L	L	L	L
SUS_ON	H	H	H	L	L	L
+1.5V_SUS	H	H	H	L	L	L
S5_ON	H	H	L	H	L	L
+5V_S5	H	H	L	H	L	L
+3V_S5	H	H	L	H	L	L
EC_WAKE_ON	H	H	H	H	H	L
+3V_WAKE	H	H	H	H	H	L
+5V_WAKE	H	H	H	H	H	L
DEEP_EC_EN	H	H	H	H	L	L
+3V_S5_DSW	H	H	H	H	L	L
+3V_SUS	H	H	L	L	L	L